

TL/G/10038-35

**DESCRIPTION**

Process 70 is a non-overlay, double-diffused, gold doped, silicon epitaxial device. Complement to Process 25.

**APPLICATION**

This device was designed primarily for high speed saturated switching applications to currents of 1A.

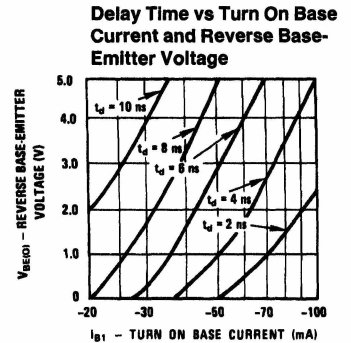
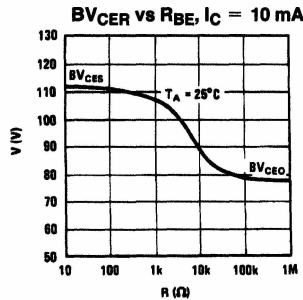
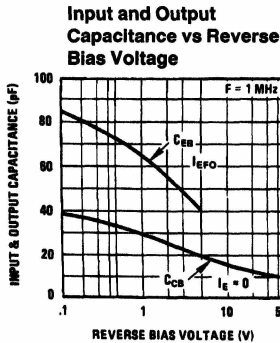
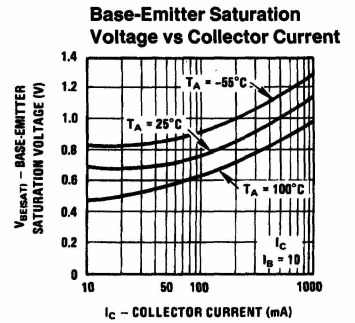
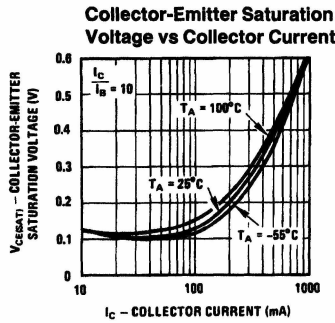
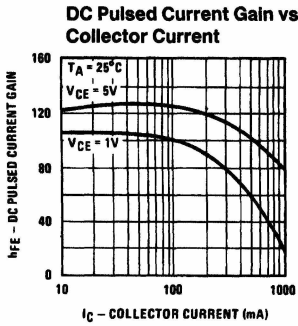
**PRINCIPAL DEVICE TYPES**
**TO-39 EBC:** 2N3467

**TO-237 EBC:** TN3467

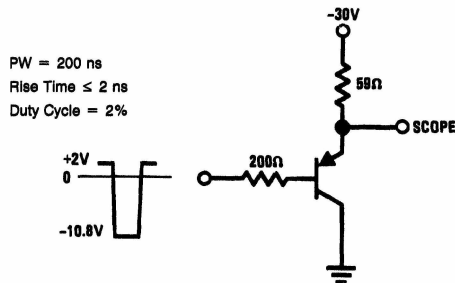
**TO-116:** MPQ3467

**ELECTRICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$ )**

Symbol	Conditions	Min	Typ	Max	Units
$t_{ON}$	$I_C = 500 \text{ mA}$ , $I_{B1} = 50 \text{ mA}$ (Figure 1)		20	40	ns
$t_{OFF}$	$I_C = 500 \text{ mA}$ , $I_{B2} = 50 \text{ mA}$ (Figure 2)		60	90	ns
$C_{ob}$	$V_{CB} = -10\text{V}$		15	20	pF
$C_{ib}$	$V_{EB} = -0.5\text{V}$			80	pF
$h_{FE}$	$I_C = 100 \text{ mA}$ , $V_{CE} = -1\text{V}$ $I_C = 500 \text{ mA}$ , $V_{CE} = -1\text{V}$ $I_C = 1\text{A}$ , $V_{CE} = -1\text{V}$	40 30 15	100	200 120	
$V_{CE(SAT)}$	$I_C = 150 \text{ mA}$ , $I_B = 15 \text{ mA}$ $I_C = 500 \text{ mA}$ , $I_B = 50 \text{ mA}$ $I_C = 1\text{A}$ , $I_B = 100 \text{ mA}$			0.3 0.6 1.0	V V V
$V_{BE(SAT)}$	$I_C = 150 \text{ mA}$ , $I_B = 50 \text{ mA}$ $I_C = 500 \text{ mA}$ , $I_B = 50 \text{ mA}$ $I_C = 1\text{A}$ , $I_B = 100 \text{ mA}$			1.2 1.2 1.7	V V V
$BV_{CEO}$	$I_C = 10 \text{ mA}$	40			V
$BV_{CBO}$	$I_C = 100 \mu\text{A}$	50			V
$BV_{EBO}$	$I_E = 10 \mu\text{A}$	6			V
$I_{CBO}$	$V_{CB} = 30\text{V}$			100	nA
$I_{EBO}$	$V_{EB} = 4\text{V}$			100	nA
$P_D(\text{max})$					
TO-39	$T_C = 25^\circ\text{C}$	7			W
	$T_A = 25^\circ\text{C}$	1			W
TO-237	$T_C = 25^\circ\text{C}$	2			W
	$T_A = 25^\circ\text{C}$	850			mW
TO-116	$T_A = 25^\circ\text{C}$				
	(Total Dissipation)	1			W
	(Each Transistor)	600			mW
$T_J(\text{max})$	All Metal Can Parts	200			$^\circ\text{C}$
	All Plastic Parts	150			$^\circ\text{C}$

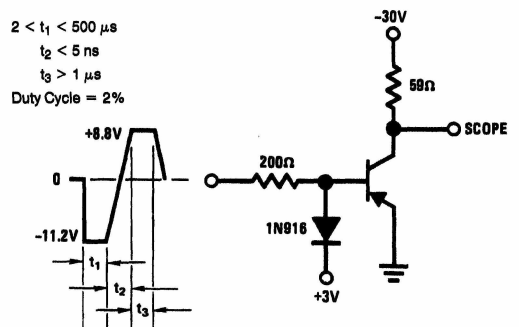


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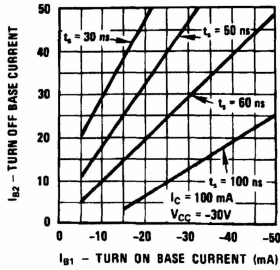
FIGURE 1.  $t_{ON}$  Equivalent Test Circuit



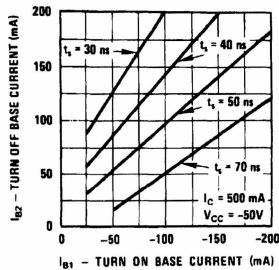
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FIGURE 2.  $t_{OFF}$  Equivalent Test Circuit

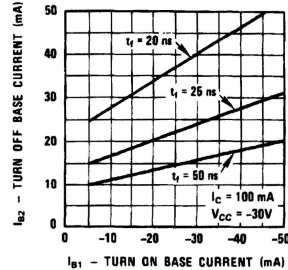
**Storage Time vs Turn On and Turn Off Base Currents**



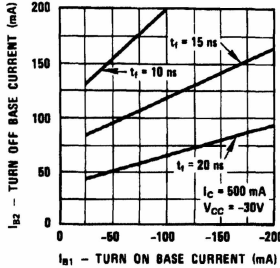
**Storage Time vs Turn On and Turn Off Base Currents**



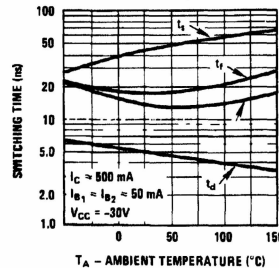
**Fall Time vs Turn On and Turn Off Base Currents**



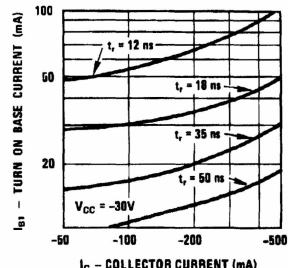
**Fall Time vs Turn On and Turn Off Base Currents**



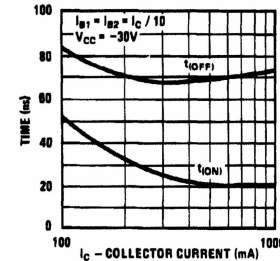
**Switching Times vs Ambient Temperature**



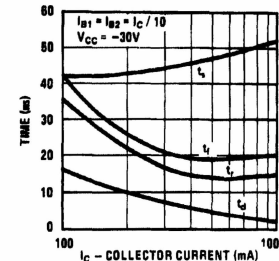
**Rise Time vs Collector Current and Turn On Base Current**



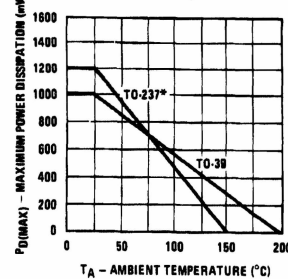
**Turn On and Turn Off Times vs Collector Current**



**Switching Times vs Collector Current**



**Maximum Power Dissipation vs Ambient Temperature**



\* One square inch of copper run

**Maximum Power Dissipation vs Case Temperature**

