



TL/G/10035-7

Gate is also backside contact

**DESCRIPTION**

Process 52 is designed primarily for low level audio and general purpose applications. These devices provide excellent performance as input stages for piezoelectric transducers or other high impedance signal sources. Their high output impedance and high voltage breakdown lend them to high gain audio and video amplifier applications. Source and drain are interchangeable.

**Electrical Characteristics** ( $T_A = 25^\circ\text{C}$ )

Symbol	Parameter	Conditions	Min	Typ	Max	Units
$BV_{GS}$	Gate-Source Breakdown Voltage	$V_{DS} = 0V, I_G = -1 \mu A$	-40	-70		V
$I_{DSS}$	Drain Saturation Current	$V_{DS} = 20V, V_{GS} = 0V$	0.2	1.5	12	mA
$g_{fs}$	Forward Transconductance	$V_{DS} = 20V, V_{GS} = 0V$	0.5	2.5	5.0	mmho
$g_{fs}$	Forward Transconductance	$V_{DS} = 20V, I_D = 200 \mu A$		700		$\mu\text{mho}$
$I_{GSS}$	Reverse Gate Leakage Current	$V_{GS} = -30V, V_{DS} = 0V$		-10	-100	$\mu A$
$r_{DS(ON)}$	Drain ON Resistance	$V_{DS} = 100 \text{ mV}, V_{GS} = 0V$	250	400	2000	$\Omega$
$V_{GS(OFF)}$	Gate Cutoff Voltage	$V_{DS} = 15V, I_D = 1 \text{ nA}$	-0.3	1.0	-8.0	V
$g_{os}$	Output Conductance	$V_{DG} = 15V, I_D = 200 \mu A$		2.0		$\mu\text{mho}$
$C_{rss}$	Feedback Capacitance	$V_{DG} = 15V, V_{GS} = 0V, f = 1 \text{ MHz}$		1.3	1.8	pF
$C_{iss}$	Input Capacitance	$V_{DG} = 15V, V_{GS} = 0V, f = 1 \text{ MHz}$		5	6	pF
$e_n$	Noise Voltage	$V_{DG} = 15V, I_D = 200 \mu A, f = 100 \text{ Hz}$		10		$nV/\sqrt{\text{Hz}}$

This process is available in the following device types. \*Denotes preferred parts.

TO-18 (NS Package 02)	TO-72 (NS Package 25)	TO-92 (NS Package 92)
2N3070	*2N3684	*J201
2N3071	*2N3685	*J202
2N3368	*2N3686	*J203
2N3369	*2N3687	PN4338
2N3370		PN4339
2N3458		*PN3684
2N3459		*PN3685
2N3460		*PN3686
*2N4338		*PN3687
*2N4339	Source and drain interchangeable.	*PN4302
*2N4340		*PN4303
*2N4341		*PN4304

