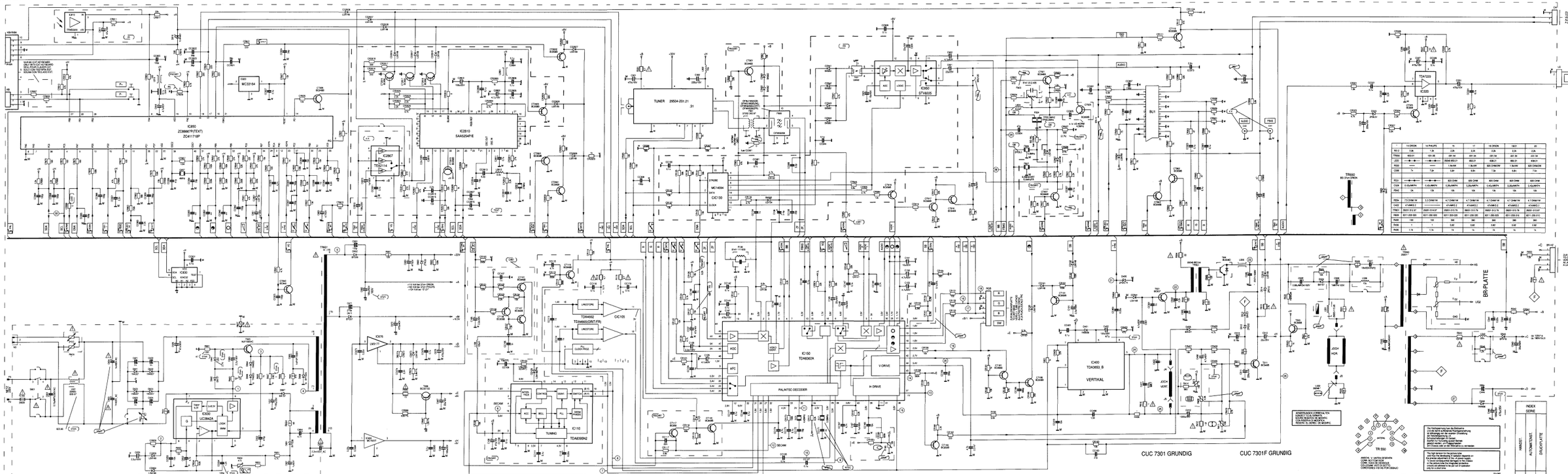Schéma synoptique général / General Circuit Diagram