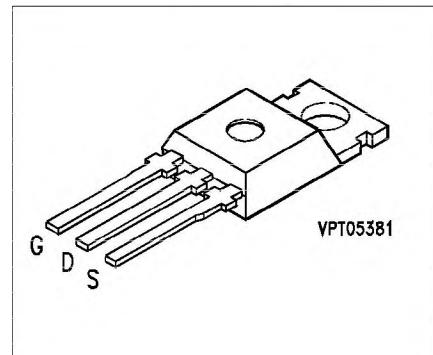


## SIPMOS® Power Transistors

**BUZ 72**  
**BUZ 72 A**

- N channel
- Enhancement mode
- Avalanche-rated



Type	$V_{DS}$	$I_D$	$R_{DS(on)}$	Package <sup>1)</sup>	Ordering Code
<b>BUZ 72</b>	100 V	10 A	0.20 $\Omega$	TO-220 AB	C67078-S1313-A2
<b>BUZ 72 A</b>	100 V	9.0 A	0.25 $\Omega$	TO-220 AB	C67078-S1313-A3

### Maximum Ratings

Parameter	Symbol	BUZ		Unit
		72	72 A	
Continuous drain current, $T_C = 25^\circ\text{C}$	$I_D$	10	9.0	A
Pulsed drain current, $T_C = 25^\circ\text{C}$	$I_{D\text{ puls}}$	40	36	
Avalanche current, limited by $T_j(\text{max})$	$I_{AR}$	10		
Avalanche energy, periodic limited by $T_j(\text{max})$	$E_{AR}$	7.9		mJ
Avalanche energy, single pulse $I_D = 10 \text{ A}$ , $V_{DD} = 25 \text{ V}$ , $R_{GS} = 25 \Omega$ $L = 885 \mu\text{H}$ , $T_j = 25^\circ\text{C}$	$E_{AS}$	59		
Gate-source voltage	$V_{GS}$	$\pm 20$		V
Power dissipation, $T_C = 25^\circ\text{C}$	$P_{\text{tot}}$	40		W
Operating and storage temperature range	$T_j$ , $T_{stg}$	– 55 ... + 150		°C

Thermal resistance, chip-case	$R_{thJC}$	≤ 3.1	K/W
DIN humidity category, DIN 40 040	–	E	–
IEC climatic category, DIN IEC 68-1	–	55/150/56	

1) See chapter Package Outlines.

**Electrical Characteristics**at  $T_j = 25^\circ\text{C}$ , unless otherwise specified.

Parameter	Symbol	Values			Unit
		min.	typ.	max.	

**Static characteristics**

Drain-source breakdown voltage $V_{GS} = 0 \text{ V}, I_D = 0.25 \text{ mA}$	$V_{(BR) DSS}$	100	-	-	V
Gate threshold voltage $V_{GS} = V_{DS}, I_D = 1 \text{ mA}$	$V_{GS(\text{th})}$	2.1	3.0	4.0	
Zero gate voltage drain current $V_{DS} = 100 \text{ V}, V_{GS} = 0 \text{ V}$ $T_j = 25^\circ\text{C}$ $T_i = 125^\circ\text{C}$	$I_{DSS}$				$\mu\text{A}$
		-	0.1	1.0	
		-	10	100	
Gate-source leakage current $V_{GS} = 20 \text{ V}, V_{DS} = 0 \text{ V}$	$I_{GSS}$	-	10	100	nA
Drain-source on-resistance $V_{GS} = 10 \text{ V}, I_D = 6 \text{ A}$ BUZ 72 BUZ 72 A	$R_{DS(\text{on})}$	-	0.15	0.2	$\Omega$
		-	0.2	0.25	

**Dynamic characteristics**

Forward transconductance $V_{DS} \geq 2 \times I_D \times R_{DS(\text{on})\text{max}}, I_D = 6 \text{ A}$	$g_{fs}$	3.0	4.3	-	S
Input capacitance $V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$	$C_{iss}$	-	400	530	pF
Output capacitance $V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$	$C_{oss}$	-	120	180	
Reverse transfer capacitance $V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$	$C_{rss}$	-	70	105	
Turn-on time $t_{\text{on}}$ , ( $t_{\text{on}} = t_{d(\text{on})} + t_i$ ) $V_{DD} = 30 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 3.0 \text{ A}, R_{GS} = 50 \Omega$	$t_{d(\text{on})}$	-	10	15	ns
	$t_i$	-	45	70	
Turn-off time $t_{\text{off}}$ , ( $t_{\text{off}} = t_{d(\text{off})} + t_f$ ) $V_{DD} = 30 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 3.0 \text{ A}, R_{GS} = 50 \Omega$	$t_{d(\text{off})}$	-	55	75	
	$t_f$	-	40	55	

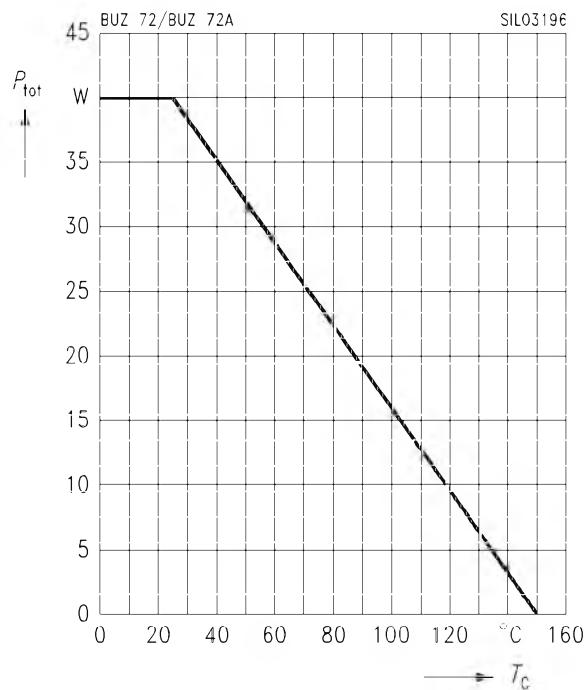
**Electrical Characteristics** (cont'd)  
at  $T_j = 25^\circ\text{C}$ , unless otherwise specified.

<b>Parameter</b>	<b>Symbol</b>	<b>Values</b>			<b>Unit</b>
		<b>min.</b>	<b>typ.</b>	<b>max.</b>	
<b>Reverse diode</b>					
Continuous reverse drain current $T_C = 25^\circ\text{C}$	$I_S$	—	—	10	A
BUZ 72		—	—	9.0	
BUZ 72 A					
Pulsed reverse drain current $T_C = 25^\circ\text{C}$	$I_{SM}$	—	—	40	
BUZ 72		—	—	36	
BUZ 72 A					
Diode forward on-voltage $I_S = 20 \text{ A}, V_{GS} = 0 \text{ V}$	$V_{SD}$	—	1.4	1.6	V
Reverse recovery time $V_R = 30 \text{ V}, I_F = I_S, di_F / dt = 100 \text{ A}/\mu\text{s}$	$t_{rr}$	—	170	—	ns
Reverse recovery charge $V_R = 30 \text{ V}, I_F = I_S, di_F / dt = 100 \text{ A}/\mu\text{s}$	$Q_{rr}$	—	0.30	—	$\mu\text{C}$

**Characteristics** at  $T_j = 25^\circ\text{C}$ , unless otherwise specified.

### Total power dissipation

$$P_{\text{tot}} = f(T_C)$$

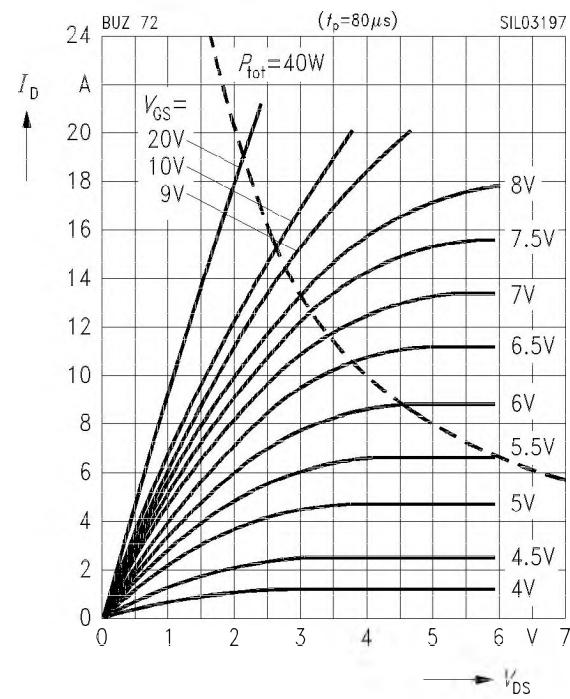


### Typ. output characteristics

$$I_D = f(V_{DS})$$

parameter:  $t_p = 80 \mu\text{s}$

**BUZ 72**

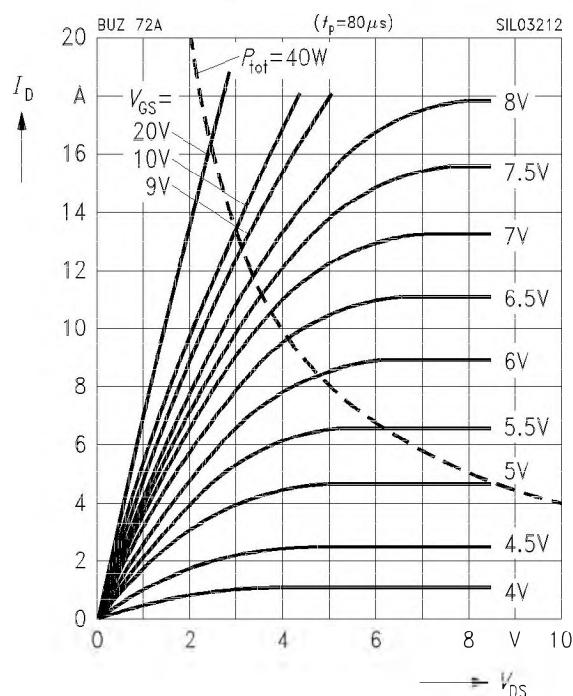


### Typ. output characteristics

$$I_D = f(V_{DS})$$

parameter:  $t_p = 80 \mu\text{s}$

**BUZ 72 A**

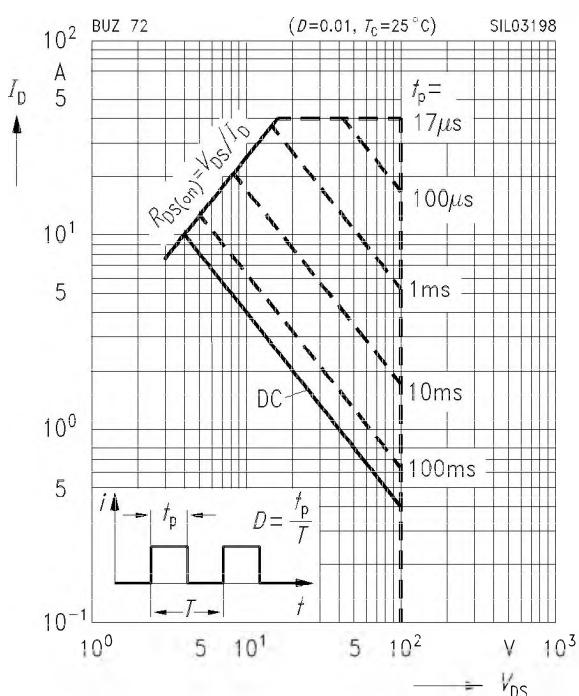


### Safe operating area

$$I_D = f(V_{DS})$$

parameter:  $D = 0.01$ ,  $T_C = 25^\circ\text{C}$

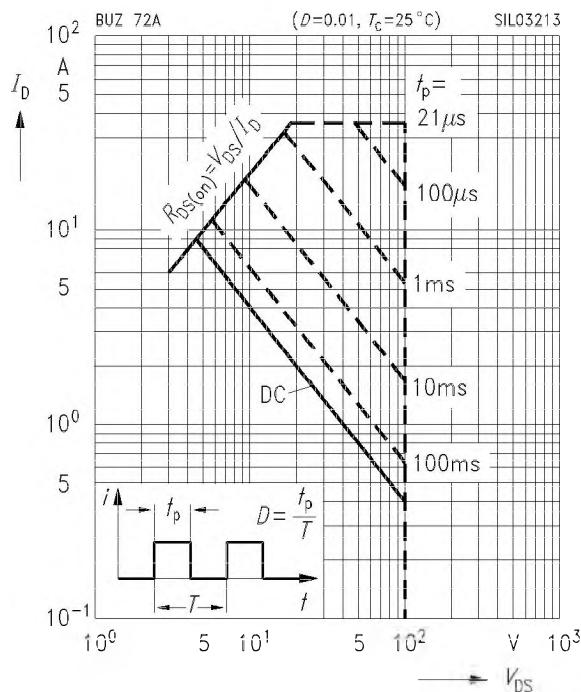
**BUZ 72**



### Safe operating area

$$I_D = f(V_{DS})$$

parameter:  $D = 0.01$ ,  $T_C = 25^\circ C$

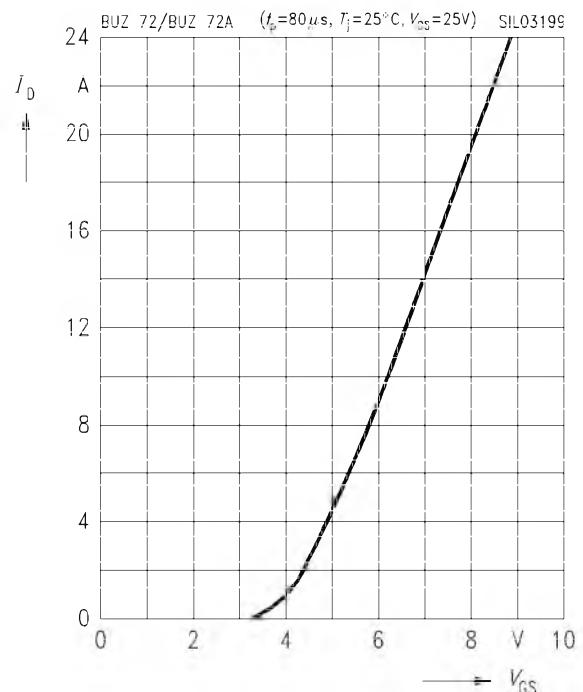


### BUZ 72 A

### Typ. transfer characteristics

$$I_D = f(V_{GS})$$

parameter:  $t_p = 80 \mu s$ ,  $V_{DS} = 25 V$

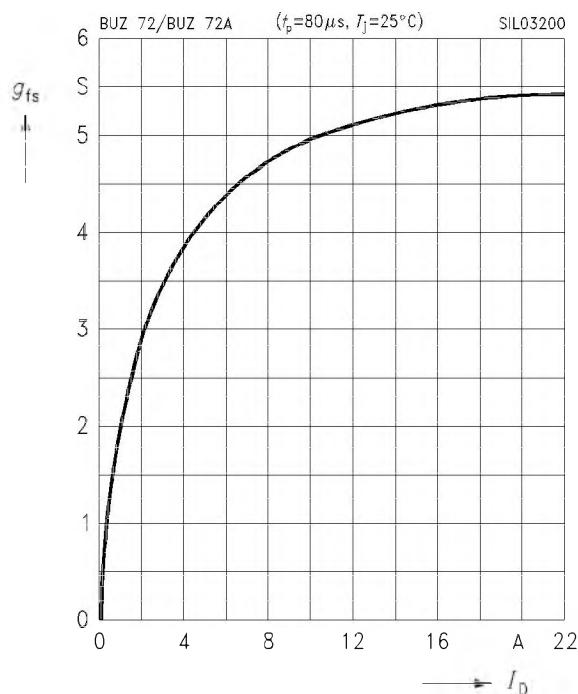


### BUZ 72

### Typ.forward transconductance

$$g_{fs} = f(I_D)$$

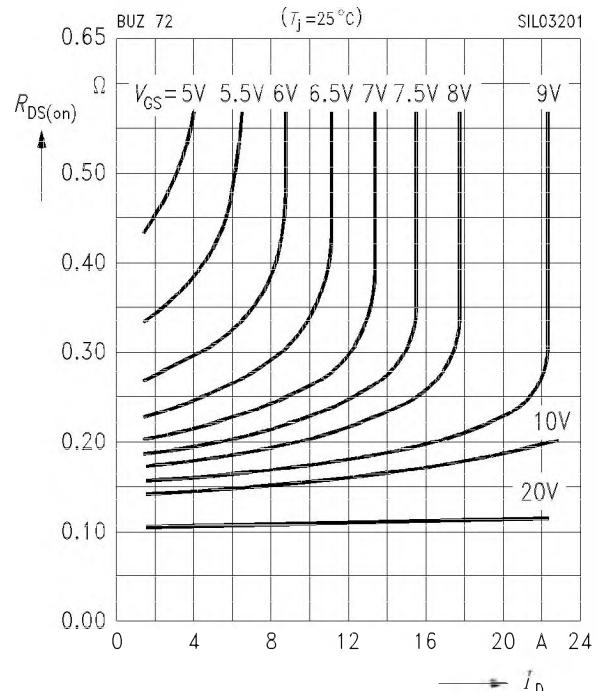
parameter:  $t_p = 80 \mu s$



### Typ. drain-source on-resistance

$$R_{DS(on)} = f(I_D)$$

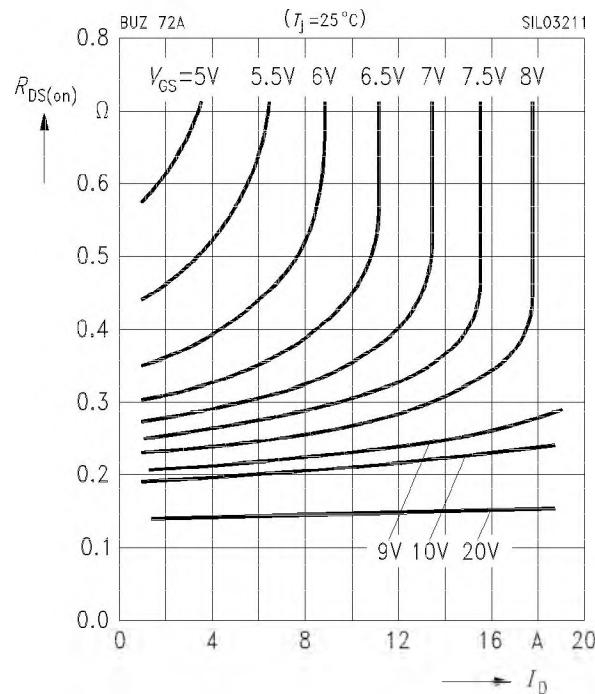
parameter:  $V_{GS}$



### Typ. drain-source on-resistance

$$R_{DS(on)} = f(I_D)$$

parameter:  $V_{GS}$

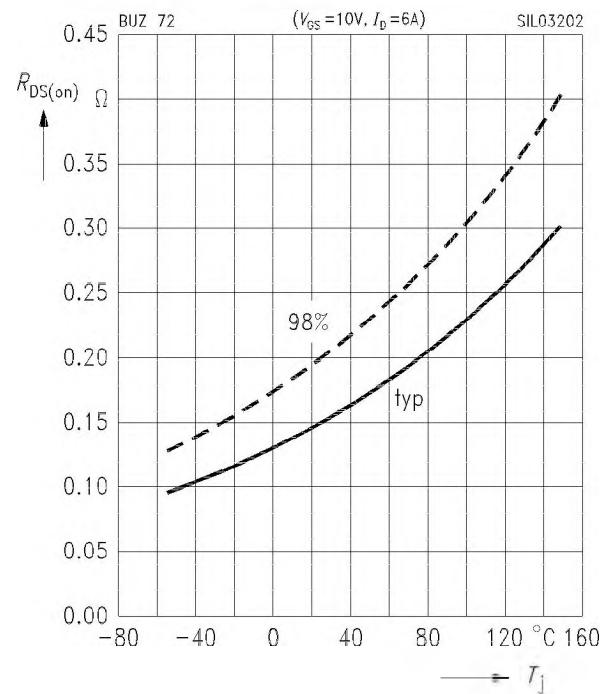


**BUZ 72 A**

### Drain-source on-resistance

$$R_{DS(on)} = f(T_J)$$

parameter:  $I_D = 6 A$ ,  $V_{GS} = 10 V$ , (spread)

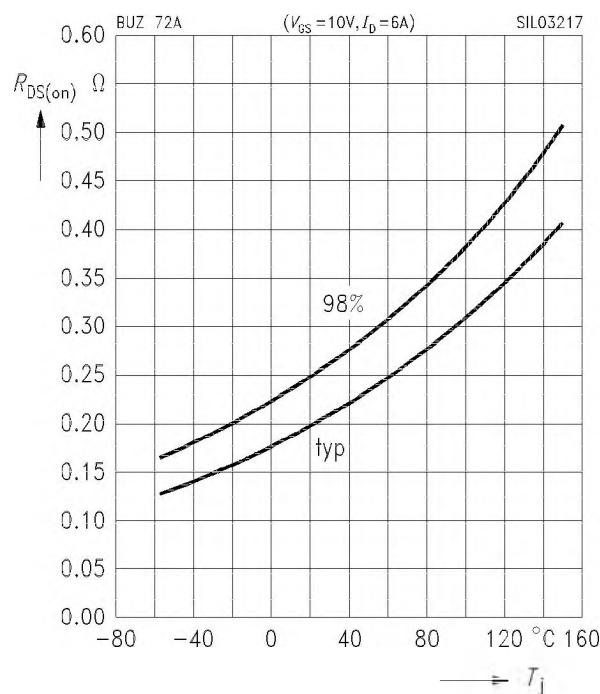


**BUZ 72**

### Drain-source on-resistance

$$R_{DS(on)} = f(T_J)$$

parameter:  $I_D = 6 A$ ,  $V_{GS} = 10 V$ , (spread)

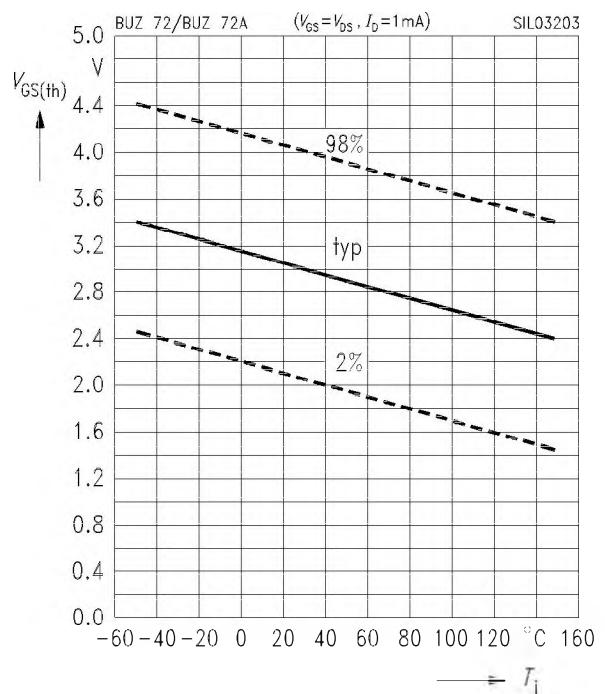


**BUZ 72 A**

### Gate threshold voltage

$$V_{GS(th)} = f(T_J)$$

parameter:  $V_{GS} = V_{DS}$ ,  $I_D = 1 mA$ , (spread)

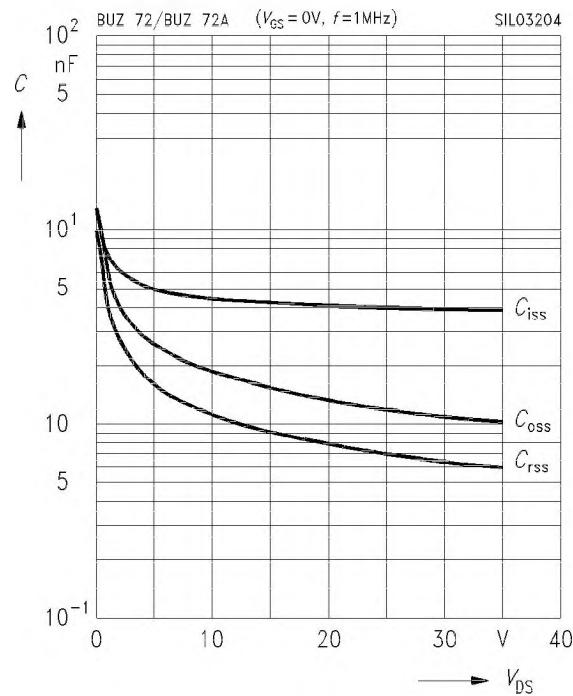


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### Typ. capacitances

$$C = f(V_{DS})$$

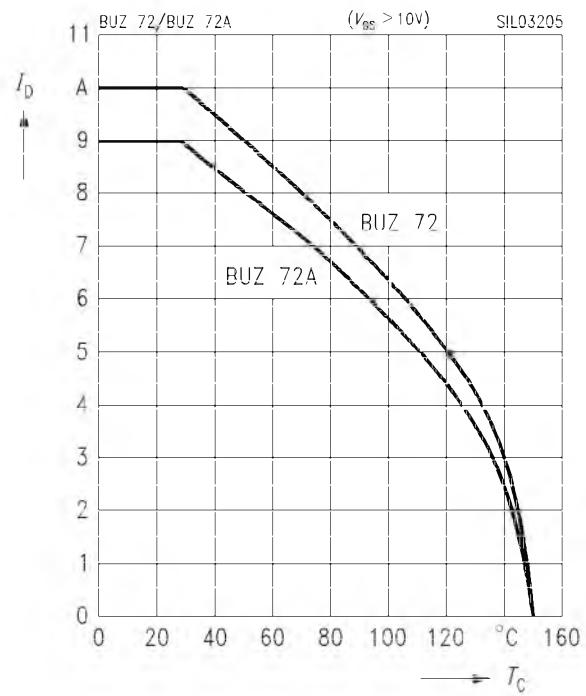
parameter:  $V_{GS} = 0 \text{ V}$ ,  $f = 1 \text{ MHz}$



### Drain current

$$I_D = f(T_C)$$

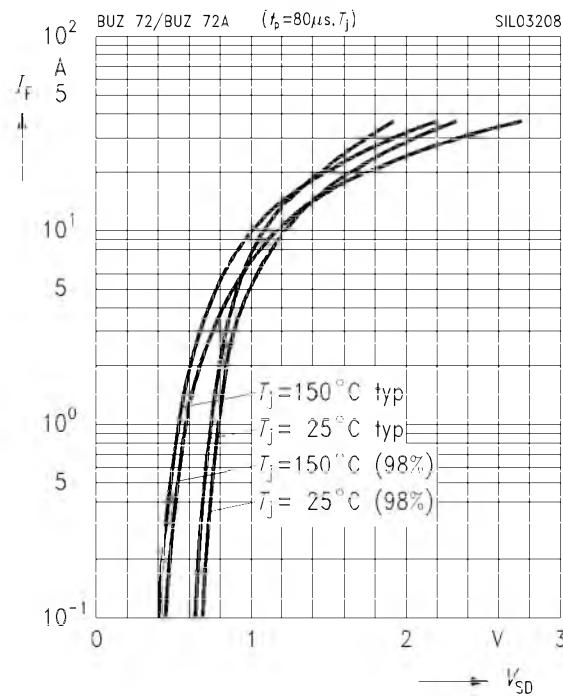
parameter:  $V_{GS} \geq 10 \text{ V}$



### Forward characteristics of reverse diode

$$I_F = f(V_{SD})$$

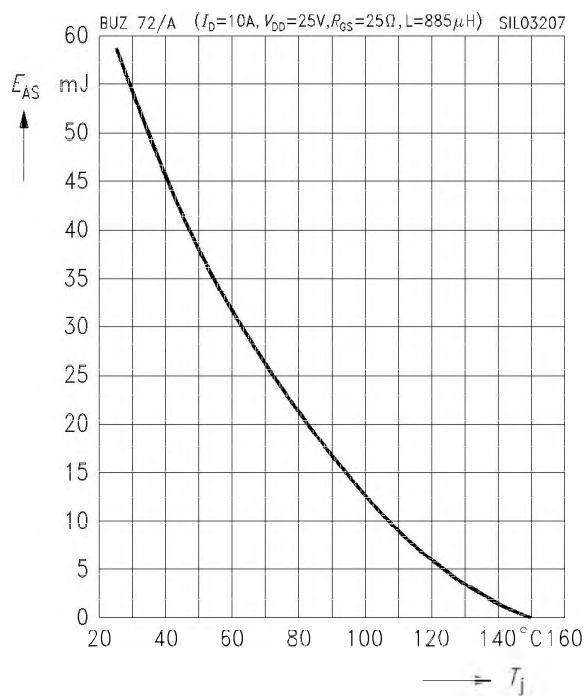
parameter:  $T_i$ ,  $t_p = 80 \mu\text{s}$ , (spread)



### Avalanche energy $E_{AS} = f(T_j)$

parameter:  $I_D = 10 \text{ A}$ ,  $V_{DD} = 25 \text{ V}$

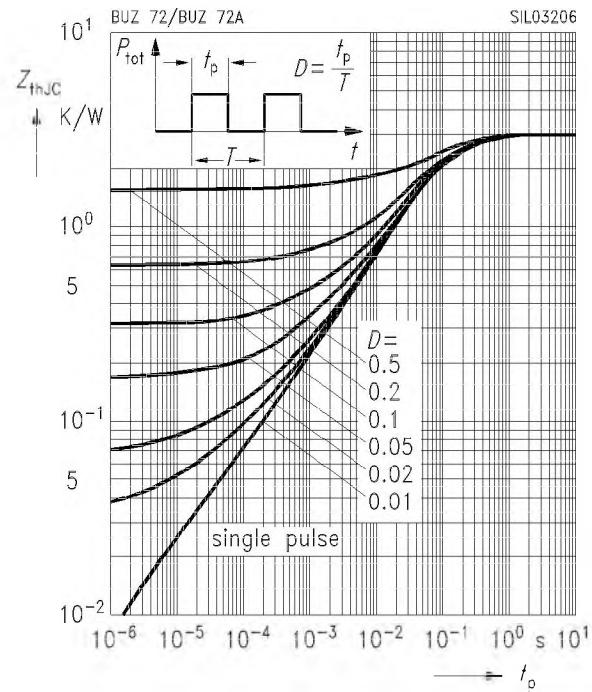
$$R_{GS} = 25 \Omega, L = 885 \mu\text{H}$$



### Transient thermal impedance

$$Z_{\text{thJC}} = f(t_p)$$

parameter:  $D = t_p / T$



### Typ. gate charge

$$V_{GS} = f(Q_{\text{Gate}})$$

parameter:  $I_{D \text{ puls}} = 21 \text{ A}$

