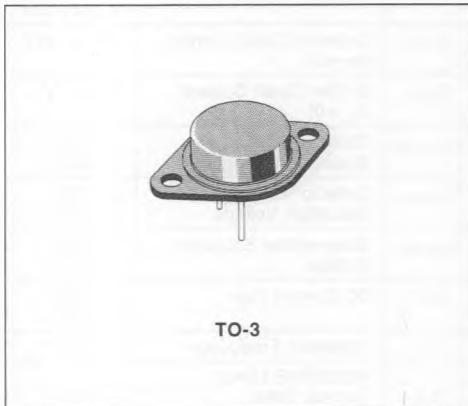


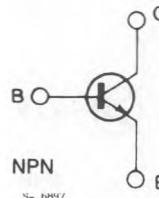
HIGH CURRENT, HIGH SPEED, HIGH POWER TRANSISTOR

ADVANCE DATA

- HIGH CURRENT
- HIGH SWITCHING SPEED
- HIGH POWER
- GOOD SOA
- GOOD RBSOA



INTERNAL SCHEMATIC DIAGRAM



DESCRIPTION

The BUR20 is a silicon multiepitaxial planar NPN transistor in modified Jedec TO-3 metal case, intended for use in switching and linear low voltage, high current applications in military and industrial equipments.

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V_{CBO}	Collector-base Voltage ($I_E = 0$)	200	V
V_{CEX}	Collector-emitter Voltage ($V_{BE} = -1.5V$)	200	V
V_{CEO}	Collector-emitter Voltage ($I_B = 0$)	125	V
V_{EBO}	Emitter-base Voltage ($I_C = 0$)	7	V
I_C	Collector Current	50	A
I_{CM}	Collector Peak Current ($t_p < 10ms$)	75	A
I_B	Base Current	15	A
P_{tot}	Total Dissipation at $T_c < 25^\circ C$	250	W
T_{stg}	Storage Temperature	-65 to 200	°C
T_j	Max. Operating Junction Temperature	200	°C

THERMAL DATA

$R_{th\ i-case}$	Thermal Resistance Junction-case	max	0.7	$^{\circ}\text{C/W}$
------------------	----------------------------------	-----	-----	----------------------

ELECTRICAL CHARACTERISTICS ($T_{case} = 25^{\circ}\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Conditions		Min.	Typ.	Max.	Unit
I_{CEX}	Collector Cutoff Current	$V_{CE} = 200\text{V}$	$V_{BE} = -1.5\text{V}$			500 6	μA mA
I_{CEO}	Collector Cutoff Current ($I_B = 0$)	$V_{CE} = 125\text{V}$				1	mA
I_{EBO}	Emitter Cutoff Current ($I_C = 0$)	$V_{EB} = 7\text{V}$				1	mA
$V_{CEO(sus)}^*$	Collector Emitter Sustaining Voltage	$I_C = 0.2\text{A}$	$L = 25\text{mH}$	125			V
$V_{CE(sat)}^*$	Collector-emitter Saturation Voltage	$I_C = 25\text{A}$ $I_C = 50\text{A}$	$I_B = 2\text{A}$ $I_B = 5\text{A}$			1 1.5	V V
$V_{BE(sat)}^*$	Base-emitter Saturation Voltage	$I_C = 25\text{A}$ $I_C = 50\text{A}$	$I_B = 2\text{A}$ $I_B = 5\text{A}$			2 2.5	V V
h_{FE}^*	DC Current Gain	$I_C = 25\text{A}$ $I_C = 50\text{A}$	$V_{CE} = 2\text{V}$ $V_{CE} = 4\text{V}$	15 10		60	
f_T	Transition Frequency	$I_C = 1\text{A}$	$V_{CE} = 15\text{V}$	$f = 10\text{MHz}$	20		MHz
t_{on} t_s t_f	RESISTIVE LOAD Turn-on Time Storage Time Fall Time	$I_C = 50\text{A}$ $V_{CC} = 60\text{V}$ $t_p = 10\mu\text{s}$	$ I_{B1} = - I_{B2} = 5\text{A}$ $V_{BB} = -6\text{V}$			1.5 1.2 0.3	μs μs μs

* Pulsed : pulse duration = 300 μs , duty cycle = 1.5%.