

SWITCHING  
N-CHANNEL POWER MOS FET  
INDUSTRIAL USE

## DESCRIPTION

The 2SK3324 is N-Channel MOS FET device that features a Low gate charge and excellent switching characteristics, and Designed for high voltage applications such as switching power supply, AC adapter.

## FEATURES

- Low gate charge :  
Q<sub>G</sub> = 32 nC TYP. (V<sub>DD</sub> = 450 V, V<sub>GS</sub> = 10 V, I<sub>D</sub> = 6.0 A)
- Gate voltage rating : ±30 V
- Low on-state resistance :  
R<sub>DS(on)</sub> = 2.8 Ω MAX. (V<sub>GS</sub> = 10 V, I<sub>D</sub> = 3.0 A)
- Avalanche capability ratings

ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub> = 25 °C)

|   |                       |              |    |
|---|-----------------------|--------------|----|
| Drain to Source Voltage                         | V <sub>DSS</sub>      | 900          | V  |
| Gate to Source Voltage                          | V <sub>GS(AC)</sub>   | ±30          | V  |
| Drain Current (DC)                              | I <sub>D(DC)</sub>    | ±6           | A  |
| Drain Current (Pulse) <sup>Note1</sup>          | I <sub>D(pulse)</sub> | ±18          | A  |
| Total Power Dissipation (T <sub>C</sub> = 25°C) | P <sub>T</sub>        | 120          | W  |
| Total Power Dissipation (T <sub>A</sub> = 25°C) | P <sub>T</sub>        | 3.0          | W  |
| Storage Temperature                             | T <sub>stg</sub>      | -55 to + 150 | °C |
| Single Avalanche Current <sup>Note2</sup>       | I <sub>AS</sub>       | 6.0          | A  |
| Single Avalanche Energy <sup>Note2</sup>        | E <sub>AS</sub>       | 21.6         | mJ |

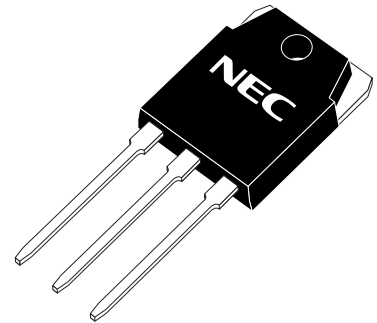
**Notes 1.** PW ≤ 10 μs, Duty cycle ≤ 1 %

**2.** Starting T<sub>ch</sub> = 25 °C, V<sub>DD</sub> = 150 V, R<sub>G</sub> = 25 Ω, V<sub>GS</sub> = 20 V → 0 V

## ORDERING INFORMATION

| PART NUMBER | PACKAGE |
|-------------|---------|
| 2SK3324     | TO-3P   |

(TO-3P)

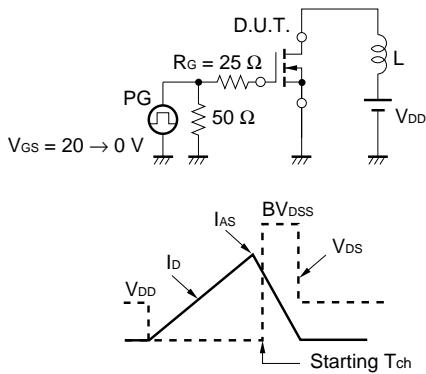


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Not all devices/types available in every country. Please check with local NEC representative for availability and additional information.

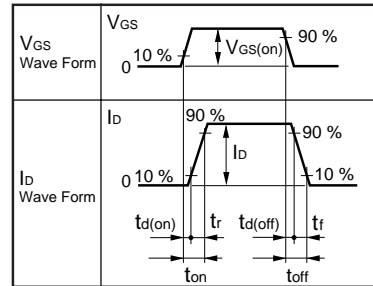
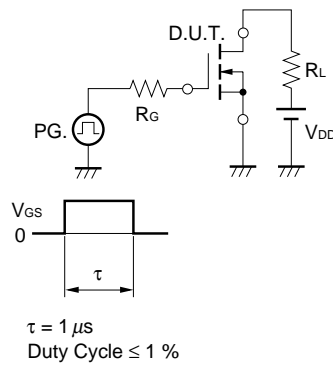
**ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25 °C)**

| CHARACTERISTICS                       | SYMBOL               | MIN. | TYP. | MAX. | UNIT | TEST CONDITIONS  |
|---------------------------------------|----------------------|------|------|------|------|--|
| Drain Leakage Current                 | I <sub>DSS</sub>     |      |      | 100  | μA   | V <sub>DS</sub> = 900 V, V <sub>GS</sub> = 0 V   |
| Gate to Source Leakage Current        | I <sub>GSS</sub>     |      |      | ±100 | nA   | V <sub>GS</sub> = ±30 V, V <sub>DS</sub> = 0 V   |
| Gate to Source Cut-off Voltage        | V <sub>GS(off)</sub> | 2.5  |      | 3.5  | V    | V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1.0 mA  |
| ★ Forward Transfer Admittance         | y <sub>fs</sub>      | 2.5  | 3.3  |      | S    | V <sub>DS</sub> = 20 V, I <sub>D</sub> = 3.0 A   |
| ★ Drain to Source On-state Resistance | R <sub>DS(on)</sub>  |      | 2.5  | 2.8  | Ω    | V <sub>GS</sub> = 10 V, I <sub>D</sub> = 3.0 A   |
| Input Capacitance                     | C <sub>iss</sub>     |      | 1000 |      | pF   | V <sub>DS</sub> = 10 V,<br>V <sub>GS</sub> = 0 V,<br>f = 1 MHz   |
| Output Capacitance                    | C <sub>oss</sub>     |      | 200  |      | pF   |  |
| Reverse Transfer Capacitance          | C <sub>rss</sub>     |      | 42   |      | pF   |  |
| Turn-on Delay Time                    | t <sub>d(on)</sub>   |      | 17   |      | ns   | V <sub>DD</sub> = 150 V,<br>I <sub>D</sub> = 3.0 A,<br>V <sub>GS(on)</sub> = 10 V,<br>R <sub>G</sub> = 10 Ω, R <sub>L</sub> = 10 Ω |
| Rise Time                             | t <sub>r</sub>       |      | 38   |      | ns   |  |
| Turn-off Delay Time                   | t <sub>d(off)</sub>  |      | 57   |      | ns   |  |
| Fall Time                             | t <sub>f</sub>       |      | 33   |      | ns   |  |
| Total Gate Charge                     | Q <sub>G</sub>       |      | 32   |      | nC   | V <sub>DD</sub> = 450 V,<br>V <sub>GS</sub> = 10 V,<br>I <sub>D</sub> = 6.0 A  |
| Gate to Source Charge                 | Q <sub>GS</sub>      |      | 5    |      | nC   |  |
| Gate to Drain Charge                  | Q <sub>GD</sub>      |      | 20   |      | nC   |  |
| Body Diode Forward Voltage            | V <sub>F(S-D)</sub>  |      | 0.9  |      | V    | I <sub>F</sub> = 6.0 A, V <sub>GS</sub> = 0 V  |
| Reverse Recovery Time                 | t <sub>rr</sub>      |      | 1.9  |      | μs   | I <sub>F</sub> = 6.0 A, V <sub>GS</sub> = 0 V,<br>di/dt = 50 A/μs  |
| Reverse Recovery Charge               | Q <sub>rr</sub>      |      | 9.0  |      | μC   |  |

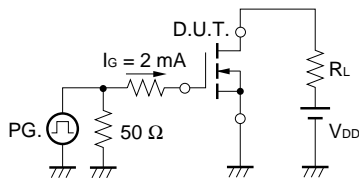
**TEST CIRCUIT 1 AVALANCHE CAPABILITY**



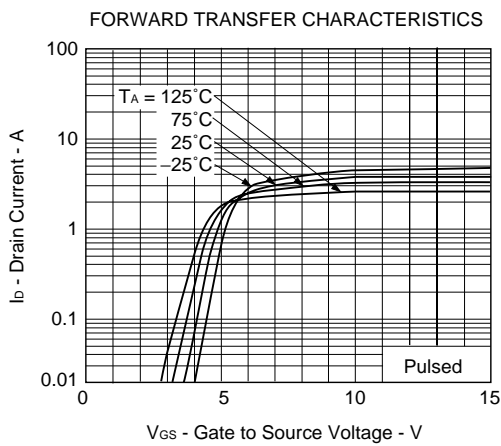
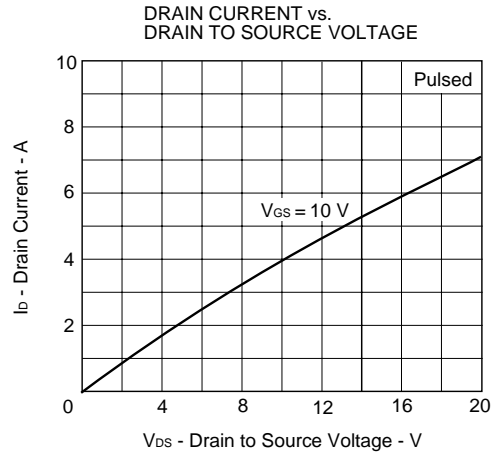
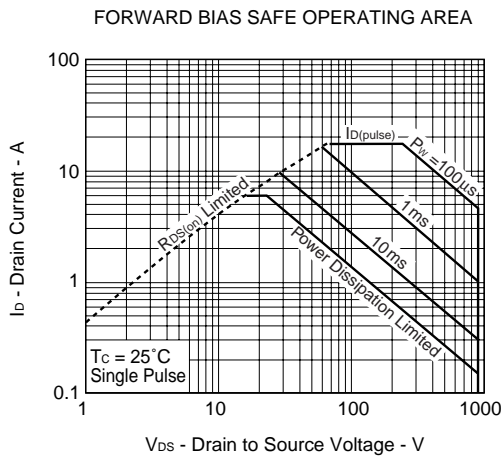
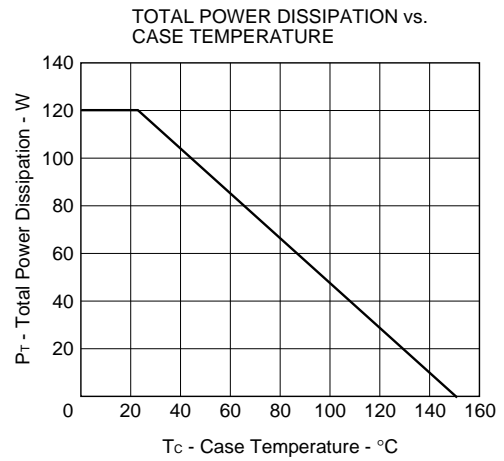
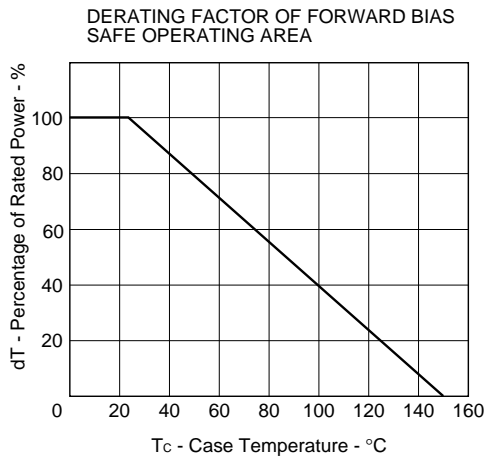
**TEST CIRCUIT 2 SWITCHING TIME**



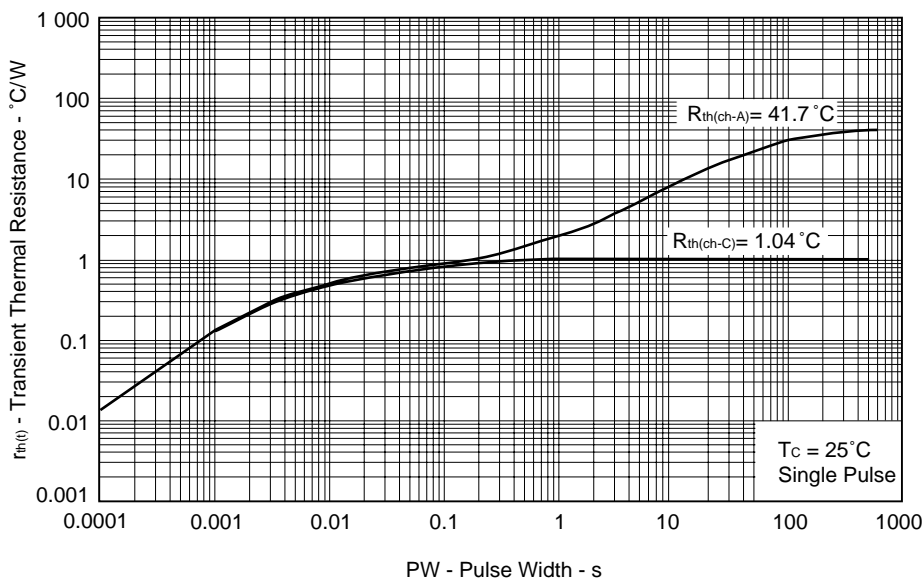
**TEST CIRCUIT 3 GATE CHARGE**



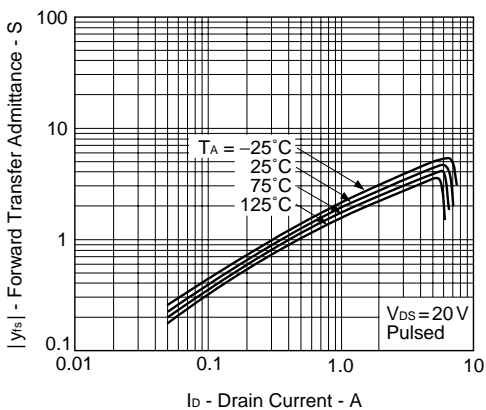
TYPICAL CHARACTERISTICS (T<sub>A</sub> = 25 °C)



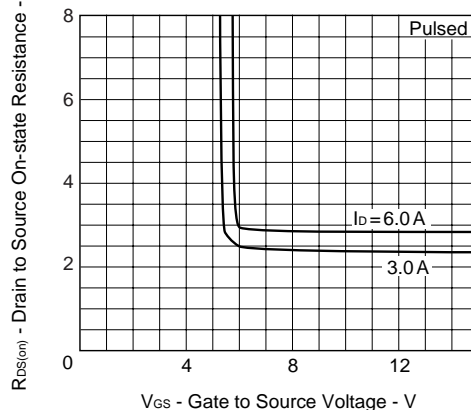
TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



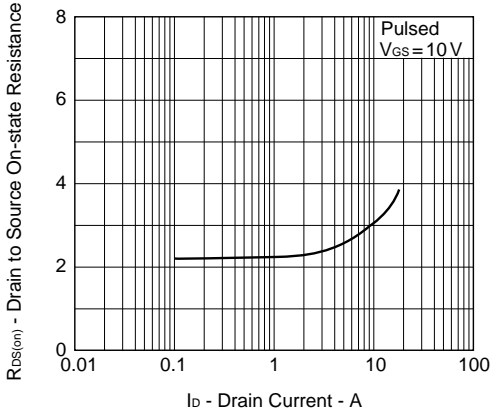
FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



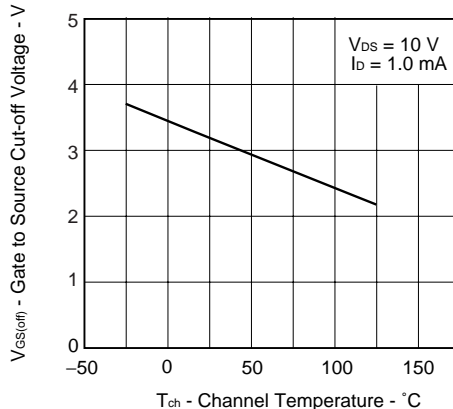
DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



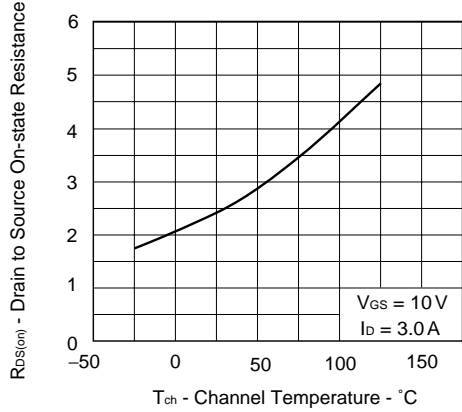
DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



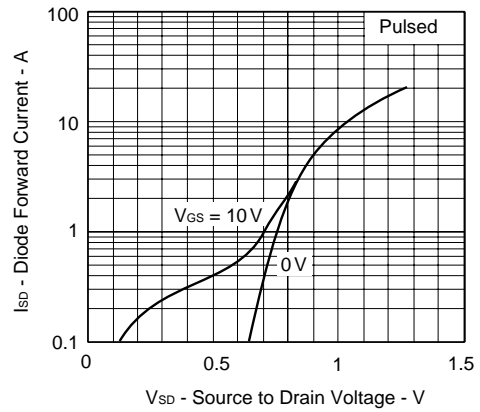
GATE TO SOURCE CUT-OFF VOLTAGE vs. CHANNEL TEMPERATURE



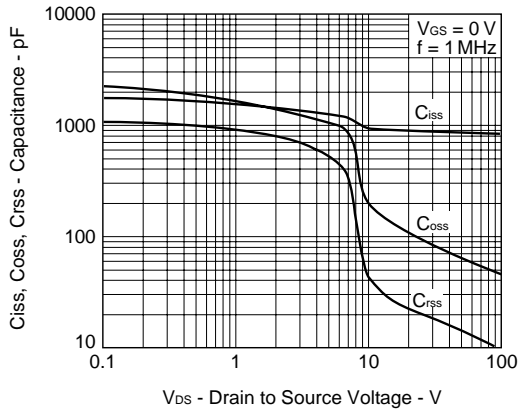
DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE



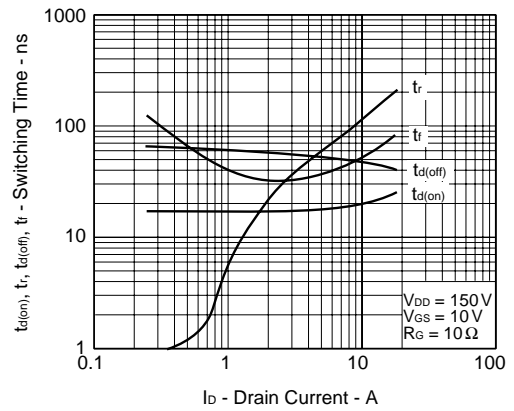
SOURCE TO DRAIN DIODE FORWARD VOLTAGE



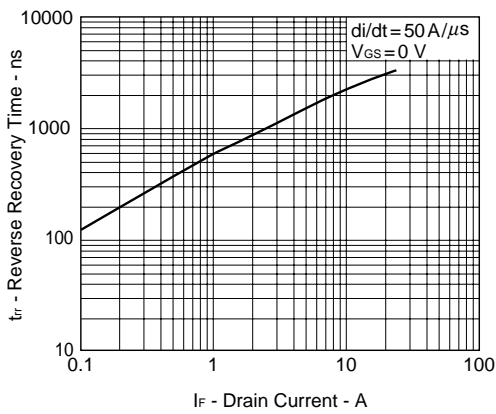
CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



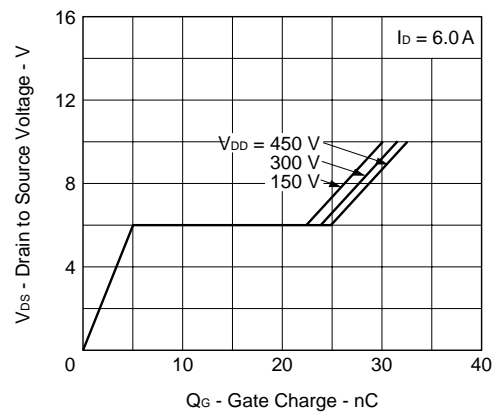
SWITCHING CHARACTERISTICS

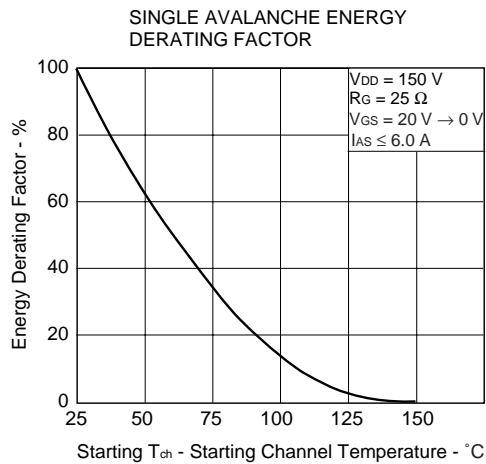
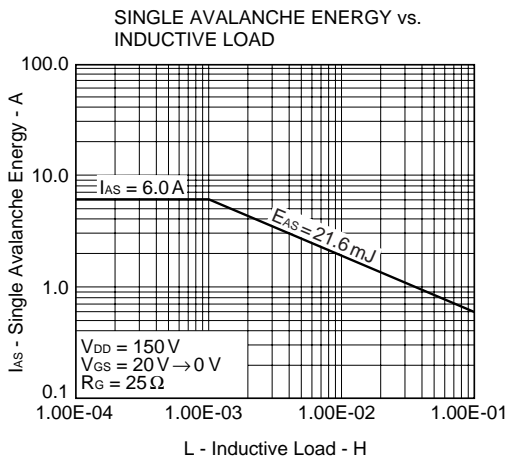


REVERSE RECOVERY TIME vs. DRAIN CURRENT



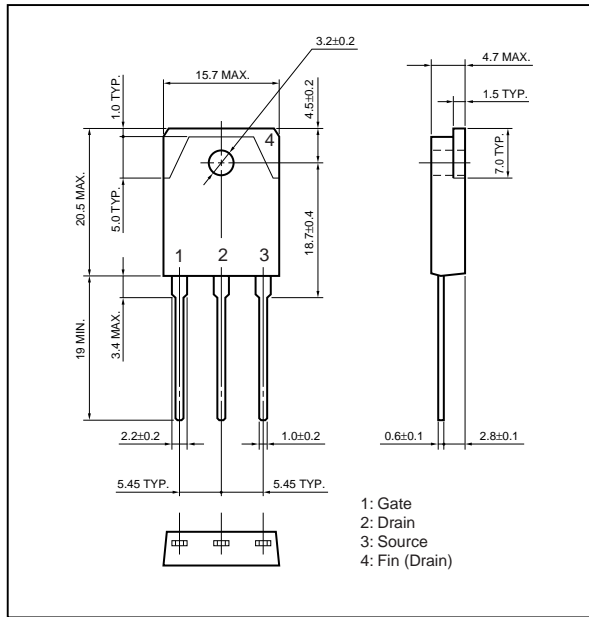
DYNAMIC INPUT/OUTPUT CHARACTERISTICS



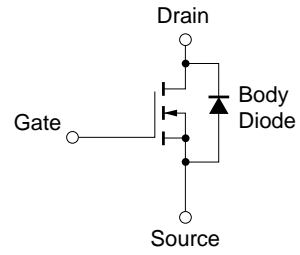


PACKAGE DRAWING (Unit : mm)

TO-3P (MP-88)



EQUIVALENT CIRCUIT



**Remark** Strong electric field, when exposed to this device, cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop generation of static electricity as much as possible, and quickly dissipate it once, when it has occurred.

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