

## TYPES 2N1671, 2N1671A, 2N1671B, 2N2160

### P-N GROWN SILICON UNIJUNCTION TRANSISTOR

#### mechanical data

The transistors are hermetically sealed in a welded package with glass-to-metal seal between case and leads. Approximate weight is one gram.

**\* ALL LEADS INSULATED FROM CASE.**

**\* OUTLINE**

**DIMENSIONS ARE IN INCHES UNLESS OTHERWISE SPECIFIED.**

**NOTES**

A. This zone is controlled for automatic handling. The variation in actual diameter within this zone shall not exceed 0.010.

B. Measured from max. diameter of the actual device.

C. The specified lead diameter applies in the zone between 0.050 and 0.250 from the base seat. Between 0.250 and 1.5 maximum of 0.021 diameter is held. Outside of these zones the lead diameter is not controlled.

\*absolute maximum ratings at 25°C free-air temperature (unless otherwise noted)

	2N1671 2N1671A 2N1671B	2N2160
Emitter-Base Reverse Voltage . . . . .	-30 v	-
Emitter-Base Reverse Voltage below 140°C Junction Temperature . . . . .	-	-30 v
Interbase Voltage . . . . .	35 v	35 v
RMS Emitter Current . . . . .	50 ma	-
DC Emitter Current . . . . .	-	70 ma
Peak Emitter Current (see Note 1) . . . . .	2 a	-
Peak Emitter Current below 140°C Junction Temperature . . . . .	-	2 a
Total Device Dissipation at 25°C Free-Air Temperature (see Note 2 & 3) . . . . .	450 mw	450 mw
Operating Temperature Range (see Note 3) . . . . .	-65°C to 140°C	-65°C to 140°C
Storage Temperature Range (see Note 4) . . . . .	-65°C to 150°C	-65°C to 150°C



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