Advance Information Supplement

Subject:	Additional Information for the VPX 322xE
Data Sheet Concerned:	VPX 322xE 6251-483-1AI, Edition Oct. 13, 1999
Supplement:	No. 1/ 6251-483-1AIS
Edition:	March 21, 2002

Changes between the VPX 322xE-A1 and the VPX 322xE-A2:

VPX 322xE-A2 21.03.02

The VPX 322xE-A2 is hardware and software compatible to the VPX 322xE-A1. The following problems have been corrected in this version.

No.	Problem	Description	Comment
1.	Noise detection is missing	The measured noise level is not availabe in the status register NOISE (FP-reg h'14).	fixed in A2
2.	Vertical timing	2 lines additional delay of sync output (VPX 3225E only)	fixed in A2
3.	Header suppression mode	not available	fixed in A2

Data Sheet Errata VPX 322xE-A1 (for Advance Information: Edition Oct. 13, 1999; 6251-483-1Al)

The following description must be added in Table 6-4 on page 77 of the Advance Information (6251-483-1AI).

FP Sub- address			Name
h'14	input noise level	read only	NOISE

The following description must be added in Table 6-5 on page 86 of the Advance Information (6251-483-1AI).

FP Sub- address	Function		Default	Name
h'153	bit[0]	polarity of the field information 0 odd high 1 even high	0	OEPOL

The following change has been made to Section 7.2. on page 87 of the Advance Information (6251-483-1AI):

The VPX 322xE only supports an 8-bit programmable output port B[7:0] if the device uses only port A[7:0] for video data output. Input mode is not available.

Micronas page 1 of 1