UNA0235

Silicon PNP epitaxial planar transistor (3 elements) Silicon NPN epitaxial planar transistor (3 elements)

For motor drives

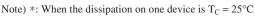
For small motor drive circuits in general

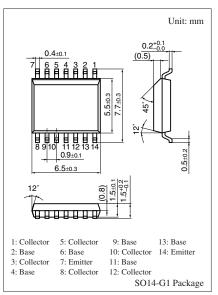
Features

- Small and lightweight
- Low power consumption
- Low-voltage drive
- With 6 elements incorporated

	Parameter	Symbol	Rating	Unit
PNP	Collector-base voltage (Emitter open)	V _{CBO}	-12	V
	Collector-emitter voltage (Base open)	V _{CEO}	-10	V
	Emitter-base voltage (Collector open)	V _{EBO}	-7	V
	Collector current	I _C	-3	А
	Peak collector current	I _{CP}	-4	А
NPN	Collector-base voltage (Emitter open)	V _{CBO}	12	V
	Collector-emitter voltage (Base open)	V _{CEO}	10	V
	Emitter-base voltage (Collector open)	V _{EBO}	7	V
	Collector current	I _C	3	А
	Peak collector current	I _{CP}	4	А
Overall	Total power dissipation *	P _T	0.5	W
	Junction temperature	Tj	150	°C
	Storage temperature	T _{stg}	-55 to +150	°C

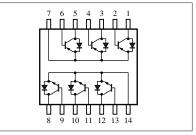
Absolute Maximum Ratings $T_a = 25^{\circ}C$





Marking Symbol: UN235

Internal Connection



\blacksquare Electrical Characteristics $T_a = 25^{\circ}C \pm 3^{\circ}C$

• PNP

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V _{CBO}	$I_{\rm C} = -10 \ \mu A, \ I_{\rm E} = 0$	-12			V
Collector-emitter voltage (Base open)	V _{CEO}	$I_{\rm C} = -1 \text{ mA}, I_{\rm B} = 0$	-10			V
Emitter-base voltage (Collector open)	V _{EBO}	$I_E = -10 \ \mu A, \ I_C = 0$	-7			V
Collector-base cutoff current (Emitter open)	I _{CBO}	$V_{CB} = -10 \text{ V}, I_E = 0$			-1	μΑ
Forward current transfer ratio *1	h _{FE}	$V_{CE} = -1 V, I_C = -0.5 A$	200		800	
Collector-emitter saturation voltage *1	V _{CE(sat)}	$I_{\rm C} = -2$ A, $I_{\rm B} = -50$ mA			- 0.45	V
Transition frequency	f _T	$V_{CB} = -6 \text{ V}, I_E = 50 \text{ mA}, f = 200 \text{ MHz}$		150		MHz
Collector output capacitance	C _{ob}	$V_{CB} = -10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$		65		pF
(Common base, input open circuited)						
Forward voltage *2	V _F	$I_F = -1 A$			-1.5	V

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors. 2. *1: Pulse measurement

*2: Application to the internal diode

• NPN

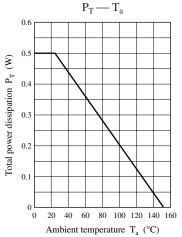
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V _{CBO}	$I_{\rm C} = 10 \ \mu A, \ I_{\rm E} = 0$	12			V
Collector-emitter voltage (Base open)	V _{CEO}	$I_{\rm C} = 1 {\rm mA}, I_{\rm B} = 0$	10			V
Emitter-base voltage (Collector open)	V _{EBO}	$I_{\rm E} = 10 \ \mu A, \ I_{\rm C} = 0$	7			V
Collector-base cutoff current (Emitter open)	I _{CBO}	$V_{CB} = 10 \text{ V}, I_E = 0$			1	μΑ
Forward current transfer ratio *1	h _{FE}	$V_{CE} = 1 V, I_C = 0.5 A$	200		800	_
Collector-emitter saturation voltage *1	V _{CE(sat)}	$I_{\rm C} = 2 \text{ A}, I_{\rm B} = 50 \text{ mA}$			0.25	V
Transition frequency	f _T	$V_{CB} = 6 \text{ V}, I_E = -50 \text{ mA}, f = 200 \text{ MHz}$		150		MHz
Collector output capacitance	C _{ob}	$V_{CB} = 10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$		50		pF
(Common base, input open circuited)						
Forward voltage *2	V _F	$I_F = 1 A$			1.5	V

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

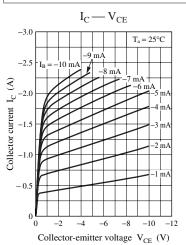
2. *1: Pulse measurement

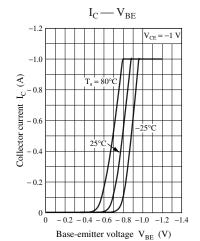
*2: Application to the internal diode

Common characteristics chart



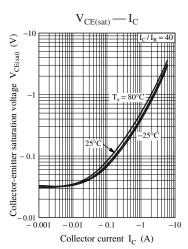
Characteristics charts of PNP transistor block

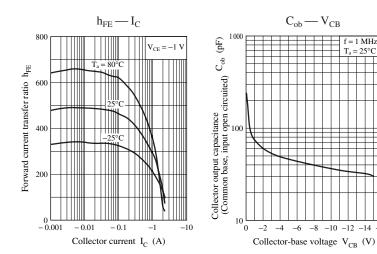




f = 1 MHz $T_a = 25^{\circ}C$

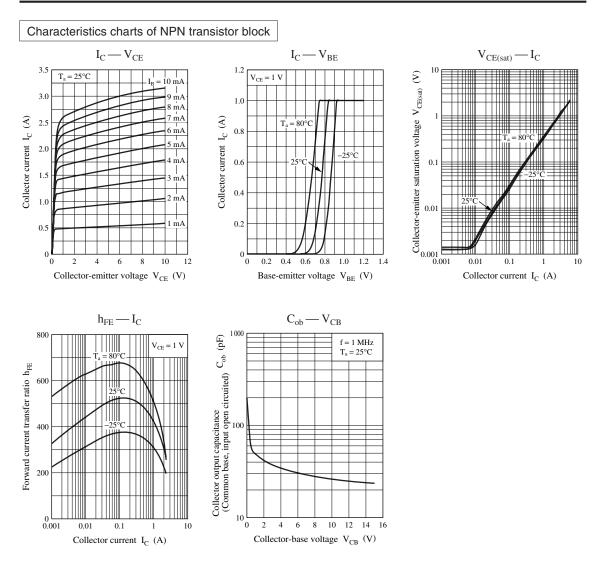
-8 -10 -12 -14 -16







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