Unit: mm

1

 $0.2^{+0.1}_{-0.0}$

UNA0234

Silicon PNP epitaxial planar transistor (4 elements) Silicon NPN epitaxial planar transistor (4 elements)

For motor drives

For Small motor drive circuits in general

■ Features

- Small and lightweight
- Low power consumption
- Low-voltage drive
- With 8 elements incorporated

■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter		Symbol	Rating	Unit	
PNP	Collector-base voltage (Emitter open)	V _{CBO}	-10	V	
	Collector-emitter voltage (Base open)	V _{CEO}	-10	V	
	Collector current	I_C	-1.5	A	
	Peak collector current	I_{CP}	-2	A	
NPN	Collector-base voltage (Emitter open)	V _{CBO}	10	V	
	Collector-emitter voltage (Base open)	V _{CEO}	10	V	
	Collector current	I_{C}	1.5	A	
	Peak collector current	I_{CP}	2	A	
Overall	Total power dissipation *	P_{T}	0.5	W	
	Junction temperature	T_{j}	150	°C	
	Storage temperature	T_{stg}	-55 to +150	°C	

1: Emitter 5: Emitter 9: Emitter 13: Emitter 2: Base 6: Base 10: Base 14: Base 3: Collector 7: Collector 11: Collector 15: Collector 4: Base 8: Base 12: Base 16: Base SO16-G1 Package Marking Symbol: UN234 Internal Connection

11 12 13 14 15 16

Note) *: When the dissipation on one device is $T_C = 25^{\circ}C$

■ Electrical Characteristics $T_a = 25$ °C ± 3 °C

• PNP

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V _{CBO}	$I_C = -10 \ \mu A, \ I_E = 0$	-10			V
Collector-emitter voltage (Base open)	V _{CEO}	$I_{\rm C} = -1 \text{ mA}, I_{\rm B} = 0$	-10			V
Collector-base cutoff current (Emitter open)	I_{CBO}	$V_{CB} = -10 \text{ V}, I_E = 0$			-1	μΑ
Collector-emitter cutoff current (Base open)	I_{CEO}	$V_{CE} = -10 \text{ V}, I_B = 0$			-2	μΑ
Forward current transfer ratio	h _{FE}	$V_{CE} = -1 \text{ V}, I_{C} = -400 \text{ mA}$	200		700	_
Collector-emitter saturation voltage	V _{CE(sat)}	$I_C = -1 \text{ A}, I_B = -25 \text{ mA}$			- 0.35	V
Base-emitter resistance *1	R _{BE}		-30%	12.5	+30%	kΩ
Forward voltage *2	V _F	$I_F = -0.5 \text{ A}$			-1.3	V

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

- 2. *1: Application to the internal resistance
 - *2: Application to the internal diode

■ Electrical Characteristics (continued) $T_a = 25$ °C ± 3 °C

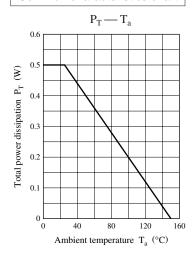
• NPN

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V _{CBO}	$I_C = 10 \ \mu A, I_E = 0$	10			V
Collector-emitter voltage (Base open)	V _{CEO}	$I_C = 1 \text{ mA}, I_B = 0$	10			V
Collector-base cutoff current (Emitter open)	I_{CBO}	$V_{CB} = 10 \text{ V}, I_E = 0$			1	μΑ
Collector-emitter cutoff current (Base open)	I _{CEO}	$V_{CE} = 10 \text{ V}, I_{B} = 0$			2	μΑ
Forward current transfer ratio	h_{FE}	$V_{CE} = 1 \text{ V}, I_{C} = 400 \text{ mA}$	200		700	_
Collector-emitter saturation voltage	V _{CE(sat)}	$I_C = 1 \text{ A}, I_B = 25 \text{ mA}$			0.25	V
Base-emitter resistance *1	R _{BE}		-30%	12.5	+30%	kΩ
Forward voltage *2	V_F	$I_F = 0.5 A$			1.3	V

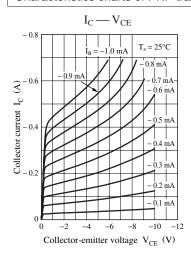
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

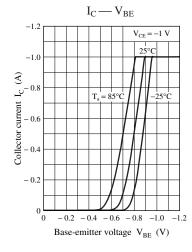
- 2. *1: Application to the internal resistance
 - *2: Application to the internal diode

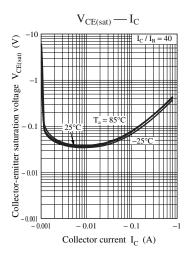
Common characteristics chart

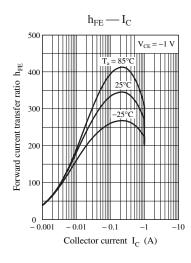


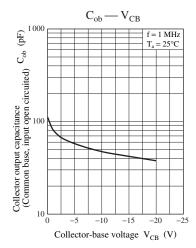
Characteristics charts of PNP transistor block



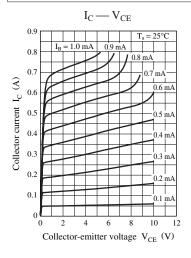


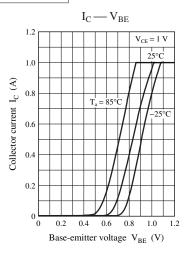


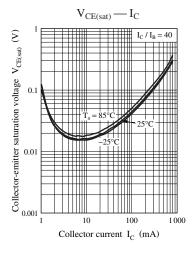


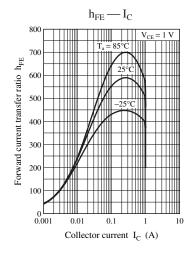


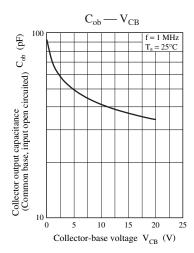
Characteristics charts of NPN transistor block











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