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10UT

3 20UT

- Meets or Exceeds the Requirements of ANSI Standards EIA/TIA-422-B and EIA/TIA-423-B and ITU Recommendations V.10 and V.11
- Operates From Single 5-V Power Supply
- Wide Common-Mode Voltage Range
- High Input Impedance
- TTL-Compatible Outputs
- High-Speed Schottky Circuitry
- 8-Pin Dual-in-Line and Small-Outline Packages
- Designed to Be Interchangeable With National DS9637A

(TOP VIEW) V_{CC} [1 8] 1IN+ 1OUT [2 7] 1IN-2OUT [3 6] 2IN+ GND [4 5] 2IN-

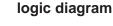
uA9637AC ... D OR P PACKAGE

description

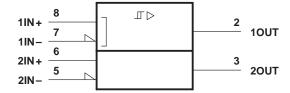
The uA9637AC is a dual differential line receiver designed to meet ANSI Standards EIA/TIA-422-B and EIA/TIA-423-B and ITU Recommendations V.10 and V.11. The line receiver utilizes Schottky circuitry and has TTL-compatible outputs. The inputs are compatible with either a single-ended or a differential-line system. This device operates from a single 5-V power supply and is supplied in an 8-pin dual-in-line package or small-outline package.

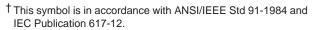
The uA9637AC is characterized for operation from 0°C to 70°C.

logic symbol[†]



2IN+







Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

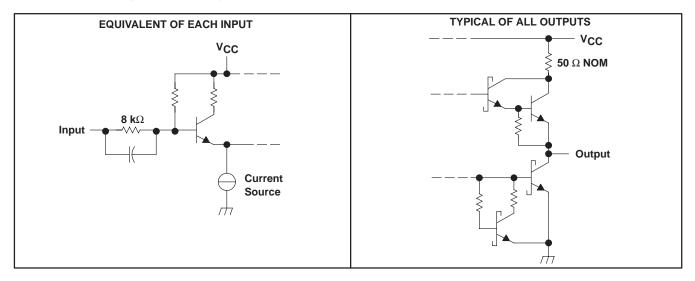
PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.



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schematics of inputs and outputs



absolute maximum ratings over operating free-air temperature range (unless otherwise noted)[†]

Supply voltage range, V _{CC} (see Note 1)	
Differential input voltage, V _{ID} (see Note 2)	
Output voltage range, V _O (see Note 1)	
Low-level output current, I _{OL}	50 mA
Continuous total dissipation	See Dissipation Rating Table
Operating free-air temperature range, T _A	0°C to 70°C
Storage temperature range, T _{stg}	–65°C to 150°C
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds	260°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

- NOTES: 1. All voltage values, except differential input voltage, are with respect to the network ground terminal.
 - 2. Differential input voltage is measured at the noninverting input with respect to the corresponding inverting input.

DISSIPATION RATING TABLE

PACKAGE	T _A ≤ 25°C POWER RATING	OPERATING FACTOR ABOVE T _A = 25°C	T _A = 70°C POWER RATING	T _A = 125°C POWER RATING
D	725 mW	5.8 mW/°C	464 mW	—
Р	1000 mW	8.0 mW/°C	640 mW	—



recommended operating conditions

	MIN	NOM	MAX	UNIT
Supply voltage, V _{CC}	4.75	5	5.25	V
Common-mode input voltage, V _{IC}			±7	V
Operating free-air temperature, TA	0		70	°C

electrical characteristics over recommended ranges of supply voltage, common-mode input voltage, and operating free-air temperature (unless otherwise noted)

PARAMETER		TEST CO	TEST CONDITIONS		TYP†	MAX	UNIT
\/ı _	Positive-going input threshold voltage	See Note 3	See Note 3			0.2	V
VIT+	Positive-going input threshold voltage	See Note 5				0.4	v
\/		See Note 3		-0.2			v
V_{IT-}	Negative-going input threshold voltage			-0.4‡			
V _{hys}	Hysteresis voltage (V _{IT+} -V _{IT-})				70		mV
VOH	High-level output voltage	V _{ID} = 0.2 V,	$I_{O} = -1 \text{ mA}$	2.5	3.5		V
VOL	Low-level output voltage	$V_{ID} = -0.2 V,$	l _O = 20 mA		0.35	0.5	V
lı İnj	Input current	$V_{CC} = 0$ to 5.5 V,	V _I = 10 V		1.1	3.25	mA
		See Note 4	$V_{I} = -10 V$		-1.6	-3.25	mA
IOS	Short-circuit output current§	$V_{O} = 0,$	$V_{ID} = 0.2 V$	-40	-75	-100	mA
ICC	Supply current	V _{ID} = -0.5 V,	No load		35	50	mA

[†] All typical values are at $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$.

[‡] The algebraic convention, in which the less positive (more negative) limit is designated as minimum, is used in this data sheet for threshold levels only.

§ Only one output should be shorted at a time, and duration of the short circuit should not exceed one second.

NOTES: 3. The expanded threshold parameter is tested with a 500- Ω resistor in series with each input.

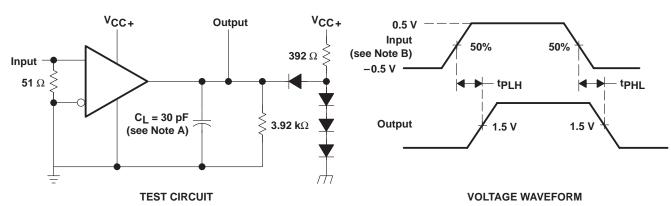
4. The input not under test is grounded.

switching characteristics, V_{CC} = 5 V, T_A = 25°C

	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
^t PLH	Propagation delay time, low- to high-level output	C _L = 30 pF, See Figure 1		15	25	ns
^t PHL	Propagation delay time, high- to low-level output			13	25	ns



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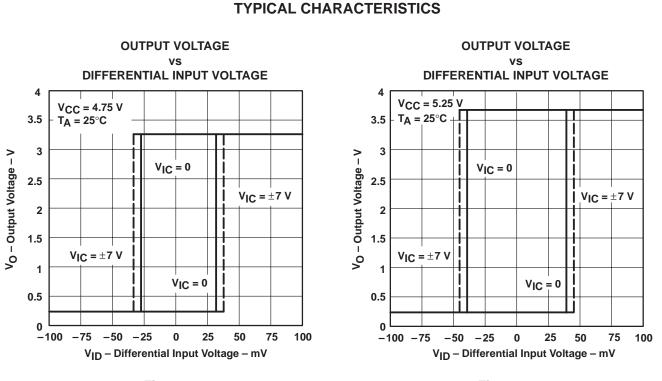


PARAMETER MEASUREMENT INFORMATION

NOTES: A. C_L includes probe and jig capacitance.

B. The input pulse is supplied by a generator having the following characteristics: $t_f \le 5$ ns, $t_f \le 5$ ns, PRR ≤ 5 MHz, duty cycle = 50%.

Figure 1. Test Circuit and Voltage Waveform

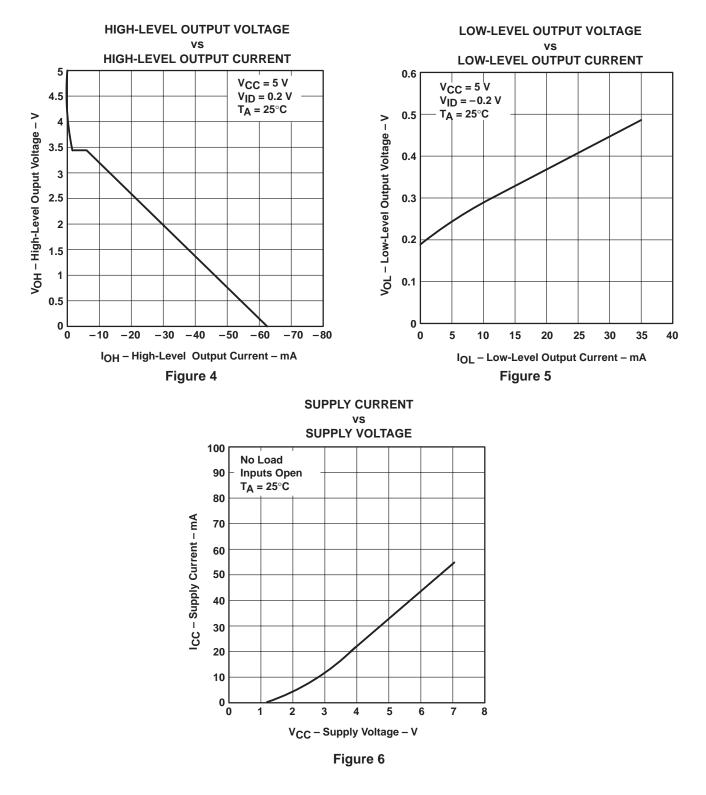








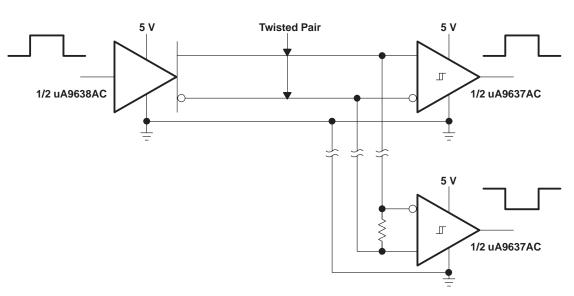
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TYPICAL CHARACTERISTICS



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APPLICATION INFORMATION

Figure 7. EIA/TIA-422-B System Applications



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