# Switch ON/OFF delay circuit for timing relays

# Description

The bipolar integrated circuits, U 6030 B and U 6031 B, are designed as switch ON/ OFF delay circuits for timing relays. They have a defined switch ON/ OFF delay time.

## Features

- Delay time range: 3.7 s to 20 h
- RC oscillator determines switching characteristics
- Relay driver with Z-diode
- Low supply current
- Load dump protection

#### rent

### Cases:

DIP 8	U 6030 B, U 6031 B
211 0	0 0000 2, 0 0001 2

SO 8	U 6030 B-FP, U 6031 B-FP

- RF interference protected
- Protection according to ISO/TR7637-1 (VDE 0839)
- U 6030 B: Switch ON delay
- U 6031 B: Switch OFF delay

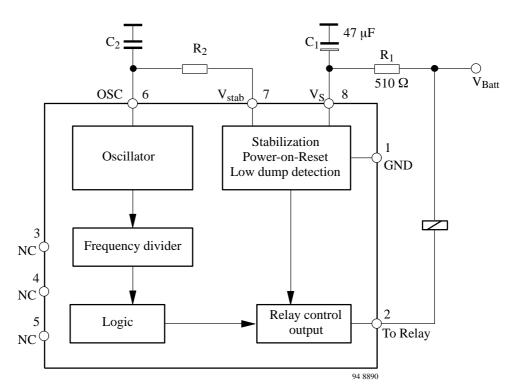


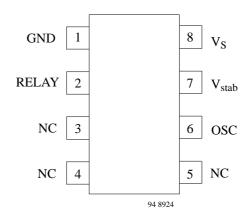
Figure 1 Block diagram with external circuit

# U 6030 B / U 6031 B

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#### **Pin Configuration**

Pin	Symbol	Function				
1	GND	Reference point, ground				
2	RELAY	Relay control output				
3	NC	Not connected				
4	NC	Not connected				
5	NC	Not connected				
6	OSC	RC oscillator input				
7	V <sub>stab</sub>	Stabilized voltage				
8	Vs	Supply voltage				



## **Functional description**

#### Power supply, Pin 8

For reasons of interference protection and surge immunity, the supply voltage (Pin 8) must be provided with an RC circuit as shown in figure 2a. Dropper resistor,  $R_1$ , limits the current in case of overvoltage, whereas  $C_1$  smoothes the supply voltage at Pin 8.

The integrated Z-diode (14 V) protects the supply voltage,  $V_S$ , therefore, the operation of the IC is possible between 6 V and 16 V, supplied by  $V_{Batt}$ .

However, it is possible to operate the integrated circuit with a 5 V supply, but it should be free of interference voltages. In this case, Pin 7 is connected to Pin 8 as shown in figure 2b, and the  $R_1C_1$  circuit is omitted.

Recommended values are:  $R_1 = 510 \Omega$ ,  $C_1 = 47 \mu F$ .

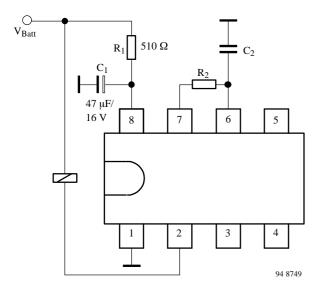


Figure 2a Basic circuit for 12 V supply and oscillator

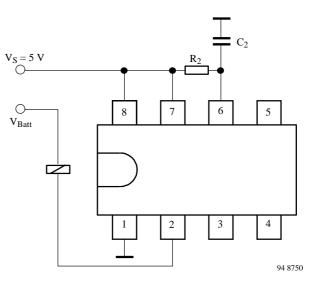


Figure 2b Basic circuit for  $V_S = 5 V$ 

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#### **Oscillator, Pin 6**

Oscillator frequency, f, is determined mainly by the  $R_2C_2$  circuit. Resistance,  $R_2$ , determines the charge time, and the integrated resistance (2 k $\Omega$ ) is responsible for discharge time. For the stability of the oscillator frequency, it is recommended that the selected  $R_2$  value be much greater than the internal resistance (2 k $\Omega$ ), because the temperature response and the tolerances of the integrated resistance are considerably greater than the external resistance value.

Oscillator frequency, f, is calculated as follows:

$$f = \frac{1}{t_1 + t_2}$$

where

 $\begin{array}{l} t_1 = charge \ time = \alpha_1 \cdot R_2 \cdot C_2 \\ t_2 = discharge \ time = \alpha_2 \cdot 2 \ k\Omega \ \cdot \ C_2 \end{array}$ 

 $\alpha_1$  and  $\alpha_2$  are constants as such  $\alpha_1 = 0.833$  and  $\alpha_2 = 1.551$  when  $C_2 = 470$  pF to 10 nF  $\alpha_1 = 0.746$  and  $\alpha_2 = 1.284$  when  $C_2 = 10$  nF to 4700 nF

Debounce time,  $t_3$ , and the delay time,  $t_d$ , depend on the oscillator frequency, f, as follows:

$$t_{3} = 6 \cdot \frac{1}{f}$$
$$t_{d} = 73728 \cdot \frac{1}{f}$$

Table 1 shows relationships between  $t_3$ ,  $t_d$ ,  $C_2$ ,  $R_2$  and frequencies from 1 Hz to 20 kHz.

#### **Relay control output**

The relay control output is an open collector Darlington circuit with an integrated 23-V Z-diode for limitation of the inductive cut-off pulse of the relay coil. The maximum static collector current must not exceed 300 mA and saturation voltage is typically 1.1 V @ 200 mA.

#### Interference voltages and load dump

The IC supply is protected by  $R_1$ ,  $C_1$ , and an integrated Z-diode, while the inputs are protected by a series resistor, integrated Z-diode and RF capacitor (refer to Figure 6).

The relay control output is protected via the integrated 23-V Z-diode in the case of short interference peaks. It is switched to conductive condition for a battery voltage of greater than approx. 40 V in the case of load dump. The output transistor is dimensioned so that it can withstand the current produced.

#### **Power-on reset**

When the operating voltage is switched on, an internal power-on reset pulse (POR) is generated which sets the logic of the circuits to a defined initial condition. The relay output is disabled.

## **Timing waveform**

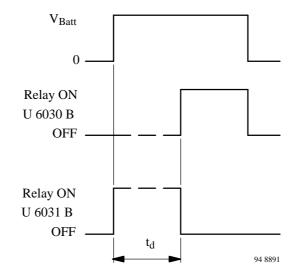


Figure 3 Behaviour of the control output as a function of supply voltage

# **Absolute Maximum Ratings**

Parameters	Symbol	Value	Unit
Operating voltage, static, 5 min	V <sub>Batt</sub>	24	V
Ambient temperature range	T <sub>amb</sub>	-40 to +125	°C
Storage temperature range	T <sub>stg</sub>	-55 to +125	°C
Junction temperature	Тj	150	°C

# **Thermal Resistance**

	Parameters	Symbol	Maximum	Unit
Junction ambient	DIP 8	T <sub>thJA</sub>	110	K/W
	SO 8	T <sub>thJA</sub>	160	K/W

# **Electrical Characteristics**

$V_{-} = -125 V T$	- 25°C matamana	maint around figure	a 2 unlaga athamuia	amaaifiad
$v_{Batt} = 13.5 v, T_a$	$_{\rm umb} = 25^{\circ} C$ , reference	e point ground, figur	e 2, unless otherwis	e specified

Parameters	Test Conditions / Pin	Symbol	Min	Тур	Max	Unit
Operating voltage	$R_1 \ge 510 \Omega$ t < 5 min t < 60 min	V <sub>Batt</sub>	6		16 24 18	V
5 V supply	Without $R_1$ , $C_1$ figure 2b Pins 7 and 8	V <sub>8</sub> , V <sub>7</sub>	4.3		6.0	V
Stabilized voltage	$V_{Batt} = 12 V$ Pin 7	V <sub>7</sub>	5.0	5.2	5.4	V
Undervoltage threshold	Power on reset	Vs	3.0		4.2	V
Supply current	All pushbuttons open, Pin 8	IS		1.3	2.0	mA
Internal Z-diode	$I_8 = 10 \text{ mA}$ Pin 8	VZ	13.5	14	16	V
<b>Relay control output</b>	Pin 2					
Saturation voltage	$I_2 = 200 \text{ mA}$ $I_2 = 300 \text{ mA}$	V <sub>2</sub>		1.2	1.5	V
Leakage current	$V_2 = 14 V$	I <sub>lkg</sub>		2	100	μΑ
Output current		I <sub>2</sub>			300	mA
Output pulse current						
Load dump pulse	$t \leq 300 \text{ ms}$	I <sub>2</sub>			1.5	A
Internal Z-diode	$I_2 = 10 \text{ mA}$	VZ	20	22	24	V
Oscillator input	f = 0.001 to 40 kHz, see table 1	Pin 6				
Internal discharge resistance	$V_6 = 5 V$	R <sub>6</sub>	1.6	2.0	2.4	kΩ
Switching voltage	Lower Upper	V <sub>6L</sub> V <sub>6H</sub>	0.9 2.8	1.1 3.1	1.4 3.5	V
Input current	$V_6 = 0 V$	-I <sub>6</sub>			1	μΑ
Switching times						
Debounce time		t3	5		7	cycles
Delay time		t <sub>d</sub>	72704		74752	cycles

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# U 6030 B / U 6031 B

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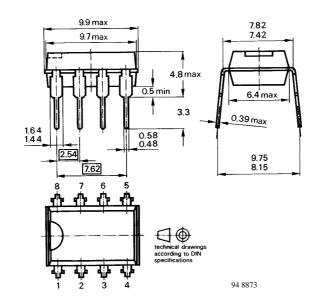
Frequency	Debounce	Delay		C <sub>2</sub>	R <sub>2</sub>		Frequency	Debounce	Delay		C <sub>2</sub>	R <sub>2</sub>
f	time t <sub>3</sub>	t,	d				f <sub>0</sub>	time t <sub>3</sub>	t <sub>d</sub>			
Hz	ms	min	s	nF	kΩ		Hz	ms	min	s	nF	kΩ
112	6000	1229	3	4700	280	1	700	9	111111	105	10	170
2	3000	614		1000	650	-	800	8		92	10	150
3	2000	410		1000	440	-	900	7		82	10	130
4	1500	307		1000	330		1000	6		74	10	120
5						-		3.00				
	1200	246		1000	260	-	2000			37	1	600
6	1000	205		1000	220	-	3000	2.00		25	1	400
7	857	176		1000	190	-	4000	1.50		18	1	300
8	750	154		1000	160		5000	1.20		15	1	240
9	667	137		1000	140		6000	1.00		12	1	200
10	600	123		1000	130		7000	.86		11	1	170
20	300	61		100	650		8000	.75		9	1	150
30	200	41		100	440		9000	.67		8	1	130
40	150	31		100	330		10000	.60		7	1	120
50	120	25		100	260	]	11000	.55		6.7	1	110
60	100	20		100	220	1	12000	.50		6.1	1	99
70	86	18		100	190	1	13000	.46		5.7	1	91
80	75	15		100	160		14000	.43		5.3	1	85
90	67	14		100	140		15000	.40		4.9	1	79
100	60	12		100	130	1	16000	.38		4.6	1	74
200	30		369	10	600	1	17000	.35		4.3	1	70
300	20		246	10	400		18000	.33		4.1	1	66
400	15		184	10	300	1	19000	.32		3.9	1	62
500	12		147	10	240	]	20000	.30		3.7	1	59
600	10		123	10	200							

#### Table 1 Oscillator frequency, debounce time, delay time. dimensioning

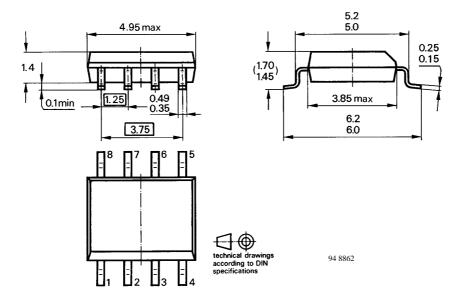
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## **Dimensions in mm**

Package: DIP 8



Package: SO 8



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