## VHF/ UHF-Tuner-IC

## Description

This tuner IC requires a power supply of 9 V and performs the function of two separate oscillators and mixers, SAWF-driver, LO output and two-state band switch.

Together with the Easy Link PLL U6220B economical 9 V tuner with excellent RF-performance can be build.

## Features

- 9 V supply voltage
- Frequency range from 48 to 860 MHz
- Band A: balanced high impedance mixer input and amplitude controlled oscillator
- Band B: balanced low impedance mixer input and symmetrical oscillator
- Balanced LO output for prescalers or PLL
- SAW filter driver with low impedance output
- Voltage regulator for stable operating characteristic


## Benefits

The integration of 2 bands and the small SSO24 package allows to design very small and low cost 9 V two-band tuners with high performance.

## Block Diagram



Figure 1. Block diagram

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Ordering Information

| Extended Type Number | Package | Remarks |
| :---: | :---: | :---: |
| U2326B-AFSG3 | SSO24 plastic package | Taped and reeled |

## Pin Description



| Pin | Symbol | Function |
| :---: | :---: | :--- |
| 1 | Osc A, base | Oscillator band A, base |
| 2 | GND, common | Ground, common |
| 3 | Osc A, coll. | Oscillator band A. collector |
| 4,9 | NC | Not connected |
| 5,8 | Osc B, base | Oscillator band B, bases |
| 6,7 | Osc B, coll. | Oscillator band B, collec- <br> tors |
| 10 | Band sw. | Dual - state band switch |
| 11,12 | SAWF, out | SAW filter driver outputs |
| 13,14 | SAWF, inp. | SAW filter driver inputs |
| 15,16 | Mix out | Mixer outputs, open collec- <br> tor |
| 17 | V S | Supply voltage $\mathrm{V}_{\mathrm{S}}$ |
| 18,19 | RF in, B | RF inputs, band B |
| 20,21 | RF in, A | RF inputs, band A |
| 22 | GND (RF) | Ground, RF part |
| 23,24 | LO out | LO outputs |

Figure 2. Pinning

## Absolute Maximum Ratings

All voltages are referred to GND, Pin 2

| Parameters | Symbol | Min. | Max. | Unit |
| :---: | :---: | :---: | :---: | :---: |
| Supply voltage Pin 17 | $\mathrm{V}_{\mathrm{S}}$ |  | 10.5 | V |
| RF inputs Pins 18 to 21 |  |  | 5.0 | V |
| IF outputs Pins 15 and 16 |  |  | 10.5 | V |
| Dual - state switch voltage Pin 10 | Vi ${ }_{\text {TRI }}$ |  | 10.5 | V |
| Junction temperature | $\mathrm{T}_{\mathrm{i}}$ |  | 150 | ${ }^{\circ} \mathrm{C}$ |
| Storage temperature | $\mathrm{T}_{\text {stg }}$ | -40 | +150 | ${ }^{\circ} \mathrm{C}$ |

## Operating Range

All voltages are referred to GND, Pin 2

| Parameters | Symbol | Min. | Typ. | Max. | Unit |  |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: |
| Supply voltage | Pins 15 to 17 | $\mathrm{~V}_{\mathrm{S}}$ | 8.1 | 9 | 9.9 | V |
| Ambient temperature |  | $\mathrm{T}_{\mathrm{amb}}$ | -20 |  | +85 | ${ }^{\circ} \mathrm{C}$ |

## Thermal Resistance

| Parameters | Test Conditions | Symbol | Typ | Unit |
| :--- | :---: | :---: | :---: | :---: |
| Junction ambient | Package SS024 soldered to PCB | $\mathrm{R}_{\text {thJA }}$ | 125 | K/W |

## Electrical Characteristics

Test conditions (unless otherwise specified): $\mathrm{V}_{\mathrm{S}}=9 \mathrm{~V} . \mathrm{T}_{\mathrm{amb}}=25^{\circ} \mathrm{C}$.
Reference point Pin 2, referred to figure 3.

| Parameters | Test Conditions / Pins | Symbol | Min. | Typ. | Max. | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Supply voltage | Pins 15 to 17 | $\mathrm{V}_{\mathrm{S}}$ | 8.1 | 9.0 | 9.9 | V |
| Supply current | Pins 15 to 17 | $\mathrm{I}_{S}$ |  | 42 |  | mA |
| Band switch |  |  |  |  |  |  |
| Voltage band A | Pin 10 | VSWA | 0 | 0 | 1.0 | V |
| Voltage band B | Pin 10 | VSWB | 3.4 | 4.0 | $\mathrm{V}_{\mathrm{S}}$ | V |
| Switching current | VSW = 5 V Pin 10 | ISW |  |  | 100 | $\mu \mathrm{A}$ |
| L.O.-output |  |  |  |  |  |  |
| L.O. Level each output | RL $=50 \Omega$ Pins 23 and 24 | PLO | -25 |  | -17 | dBm |
| SAW filter driver, $\mathrm{fi}=36 \mathrm{MHz}$ |  |  |  |  |  |  |
| Input impedance | Pins 13 and 14 | ZiSAW |  | 450 |  | $\Omega$ |
| Output impedance | Pins 11 and 12 | ZoSAW |  | 70 |  | $\Omega$ |
| Voltage gain | Pins $13,14 \rightarrow 11,12$ | GvSAW |  | 17 |  | dB |
| Band A |  |  |  |  |  |  |
| Input frequency range (note 5) | Pin 20 | fiA | 48 |  | 470 | MHz |
| Input impedance | Pin 20 | S11A |  | see fig. 4 |  |  |
| Gain (note 4) | I/P to O/P | GA |  | 28 |  | dB |
| Noise figure DSB (note 2) | $\begin{array}{r} \mathrm{I} / \mathrm{P} \text { to } \mathrm{O} / \mathrm{P}, \mathrm{fiA}=50 \mathrm{MHz} \\ \mathrm{fiA}=150 \mathrm{MHz} \end{array}$ | NF |  | $\begin{gathered} 11.5 \\ 12 \\ \hline \end{gathered}$ |  | $\begin{aligned} & \mathrm{dB} \\ & \mathrm{~dB} \end{aligned}$ |
| Input level for IM3 (note 3, interm. of 3rd order) | fiA $=71 \mathrm{MHz}$ | ViA |  | $\begin{gathered} \hline \text { (note 6) } \\ -20 \\ \hline \end{gathered}$ |  | dBm |
| IM2 (interm. of 2nd order) | $\mathrm{fiA}=71 \mathrm{MHz}$ | ViA |  | $\begin{gathered} -22 \\ \text { (note 6) } \end{gathered}$ |  | dBm |
| Band B (note 1) |  |  |  |  |  |  |
| Input frequency range (note 5) | Pins 18 and 19 | fiB | 300 |  | 860 | MHz |
| Input impedance | Pins 18 and 19 | S11B |  | see fig. 4 |  |  |
| Gain (note 4) | I/P to O/P | GB |  | 32 |  | dB |
| Noise figure DSB (note 2) | $\begin{array}{r} \mathrm{I} / \mathrm{P} \text { to } \mathrm{O} / \mathrm{P}, \mathrm{fiB}=500 \mathrm{MHz} \\ \mathrm{fiB}=800 \mathrm{MHz} \end{array}$ | NF |  | $\begin{aligned} & 10.5 \\ & 11.5 \end{aligned}$ |  | dB |
| Input level for IM3 (note 3, interm. of 3rd order) | $\mathrm{fiB}=600 \mathrm{MHz}$ | ViB |  | $\begin{gathered} \hline \text { (note 6) } \\ -25 \\ \hline \end{gathered}$ |  | dBm |

## Notes:

1. The RF input B is symmetrical driven by means of a hybrid for $180^{\circ} \mathrm{C}$ phase shifting, consequently the source impedance is $100 \Omega$. All other impedance for RF tests is $50 \Omega$.
2. The noise figure (NF) is the value for double-side-band measurement.
3. The intermodulation test (2-carrier-method) which is made on IF-center is in reference to a signal-to-IM ratio of 60 dB .
4. Gain is the ratio of the voltage at the primary coil of L6 to the available voltage at the input.
5. Depending on the oscillator's external circuitry
6. For each carrier

## Test and Principle Application Circuit



9612012
Figure 3. Test and principle application circuit

## Input Impedance Mixer Band A (S11A) and B (S11B)



Figure 4. Input impedance mixer band A (S11A) and B (S11B)

1) VHF

Normalised to $50 \Omega$, measuring range 45 to 750 MHz .
2) UHF

Normalised to $50 \Omega$, measuring range 45 to 1045 MHz . Both inputs are driven symmetrical.
The output impedance of the hybrid is $100 \Omega$, the measured levels are then calculated in reference to $50 \Omega$.

## Dimensions in mm

Package: SSO24

technical drawings according to DIN specifications

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