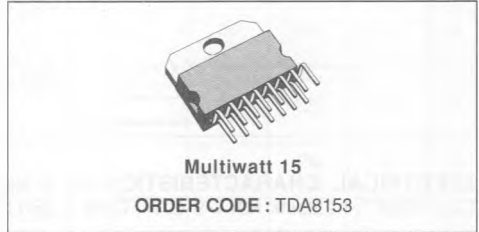


RGB VIDEO OUTPUT AMPLIFIER

- THREE INDEPENDENT VIDEO AMPLIFIERS WITH TYPICAL SR > 1000V/μs
- CRT-CATHODE SENSING OUTPUT FOR SEQUENTIAL SAMPLING
- INTERNAL GI VOLTAGE GENERATOR
- CATHODE SHORT CIRCUIT PROTECTION
- FLASHOVER PROTECTION OF THE OUTPUT STAGES
- COMPENSATES POSITIVE AND NEGATIVE TUBE LEAKAGES

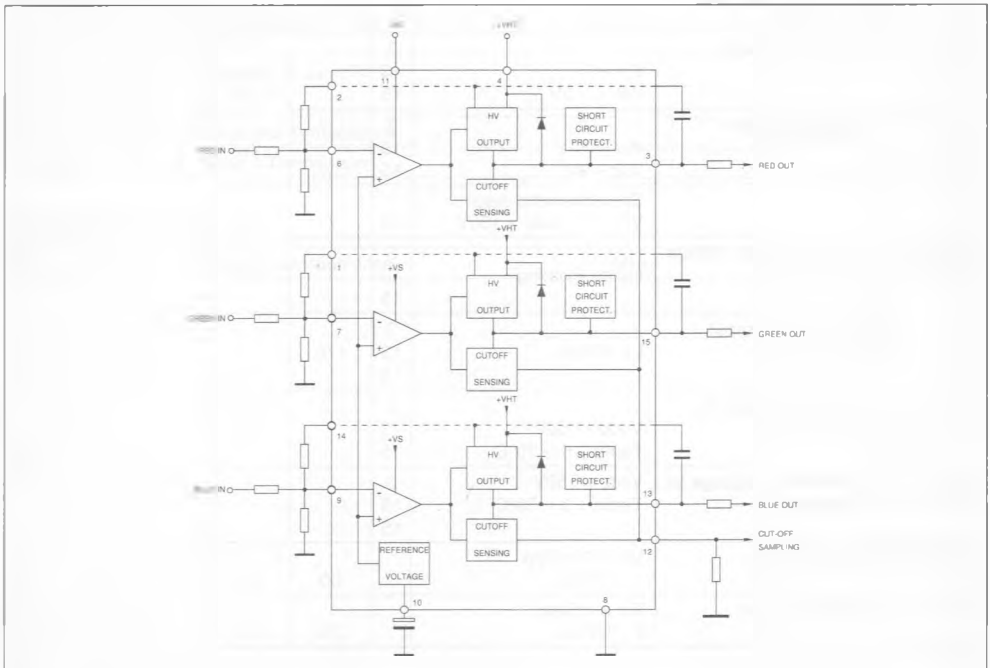


DESCRIPTION

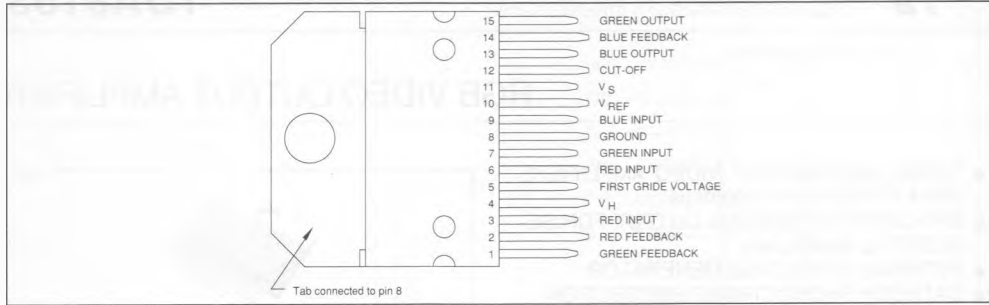
Realized with a high voltage bipolar technology, the TDA8153 is a monolithic RGB video output stage for TV color applications. It drives the CRT cathodes directly and offers a video bandwidth compatible with CCIR standards. In addition to three inde-

pendent video amplifiers, the device features an internal generator for the first grid voltage, flashover protection, cathode short circuit protection and a common cut-off sensing output for use in sequential sampling applications.

BLOCK DIAGRAM



PIN CONNECTIONS (top view)



ELECTRICAL CHARACTERISTICS (ref. to test and application circuits, $V_{HT} = 200V$, $V_S = 12V$, $CL = 10pF$ *, heatsink $R_{th} = 9^{\circ}C/W$, $T_{amb} = 25^{\circ}C$ unless otherwise specified).

| Symbol | Parameter | Test Conditions | Pin | Min. | Typ. | Max. | Unit | Fig. |
|----------------------------------|---|---|---------------|------|-------------------|-------|-----------------|------|
| V_{HT} | High Voltage Supply | | 4 | | 200 | 220 | V | 1-2 |
| V_S | Low Voltage Supply | | 11 | 10.8 | 12 | 13.2 | V | 1-2 |
| I_{HT} | Quiescent Drain Current | $V_{in} = 0$ $V_{dc} = V_{sat} H$ | 4 | | 10 | 15 | mA | 1 |
| I_S | Quiescent Drain Current | $V_{in} = 0$ $V_{dc} = V_{sat} H$ | 11 | | 10 | 17 | mA | 1 |
| V_{ref} | Reference Voltage | | 10 | 1.4 | 1.6 | 1.9 | V | 1 |
| V_{g1} | CRT G1 Voltage Supply | | 5 | | $V_S + V_{be}$ | | V | 1 |
| V_{sat} | H Output Saturation | $V_{in} = 0$ $V_{dc} = -3V$ | 3 13 15 | | V_{HT} $-3V$ | | V | 1 |
| V_{sat} | L Output Saturation | $V_{in} = 0$ $V_{dc} = 9V$ | 3 13 15 | | V_S | | V | 1 |
| I_1, I_2, I_3 | | See schematic diag. $V_{in} = 0$; $V_{dc} = 150V$ | 12 | 7 | 15 | 20 | μA | 1 |
| V_{dc} | Quiescent Output Voltage | Inputs Floating | 3 13 15 | | 123 | | V | 1 |
| V_o | Peak-to-peak Output Swing | $f = 10KHz$ | 3 13 15 | 170 | | | V _{pp} | 1 |
| $\frac{\Delta V_{dc}}{\Delta T}$ | DC Output Voltage vs. Temperature | $V_{dc} = 150V$ $T_{amb} = 0 + 70^{\circ}C$ | 3 13 15 | | 0.03 | | V/ $^{\circ}C$ | 1 |
| $\frac{\Delta V_{dc}}{\Delta T}$ | DC Differential Voltage vs. Temperature | $V_{dc} = 150V$ $T_{amb} = 0 + 70^{\circ}C$ | 3 13 15 | | | 0.015 | V/ $^{\circ}C$ | 1 |
| G_{Vo} | Open-loop Gain | $V_{in} = 50mV_{pp}$ $f = 10KHz$ | | 50 | 56 | | dB | 1 |
| G_{Vc} | Closed-loop Gain | $V_{in} = 1.5V_{pp}$ $f = 10KHz$ | | 20 | 25 | | dB | 1 |

ELECTRICAL CHARACTERISTICS (continued)

| Symbol | Parameter | Test Conditions | Pin | Min. | Typ. | Max. | Unit | Fig. |
|------------|---------------------------------|--|-----|------------------|------------------|------|------|------|
| Bw | Video Bandwidth (-3 dB) | Vobl = 125V 0dB at f = 100KHz Vo = 80Vpp 50Vpp 10Vpp | | 4.5 6.5 12 | 6.0 8.0 15 | | MHz | 2 |
| tr | Rise Time | Vo = 100Vpp ; Vobl = 150V f = 100KHz Duty Cycle = 0.5 | | | 80 | 120 | ns | 2 |
| tf | Fall Time | Vo = 100Vpp ; Vobl = 150V f = 100KHz Duty Cycle = 0.5 | | | 80 | 120 | ns | 2 |
| ΔT | Differential Rise and Fall Time | | | | | 20 | ns | 2 |
| | Overshoot | Vo = 100Vpp ; Vobl = 150V f = 100KHz Duty Cycle = 0.5 | | | | 5 | % | 2 |
| | Undershoot | Vo = 100Vpp ; Vobl = 150V f = 100KHz Duty Cycle = 0.5 | | | | 5 | % | 2 |

* CL = 10pF is the sum of the P.C. board capacitance (with socket) and the cathode capacitance of the CRT.

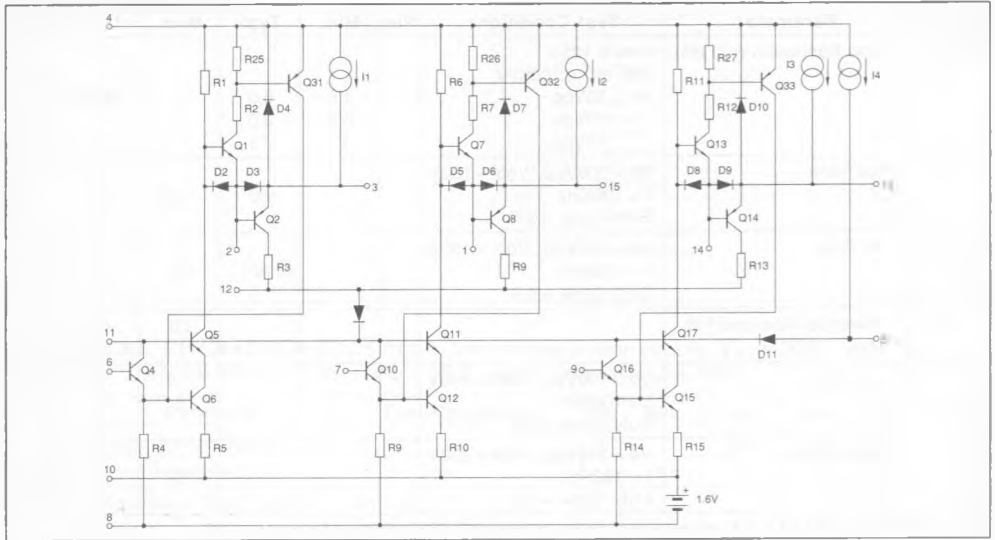
ABSOLUTE MAXIMUM RATINGS

| Symbol | Parameter | Value | Unit |
|-----------------------------------|---|----------------|------|
| V _h | High Voltage Supply | 250 | V |
| V _s | Low Voltage Supply | 35 | V |
| P _{tot} | Power Dissipation at T _{case} = 90°C | 20 | W |
| V _i | Input Voltage | V _s | |
| T _{sig} , T _j | Storage and Junction Temperature | -25 to 150 | °C |
| T _{op} | Operating Ambient Temperature | 0 to 70 | °C |

THERMAL DATA

| | | | | |
|------------------------|----------------------------------|-----|---|------|
| R _{th j-case} | Thermal Resistance Junction-case | Max | 3 | °C/W |
|------------------------|----------------------------------|-----|---|------|

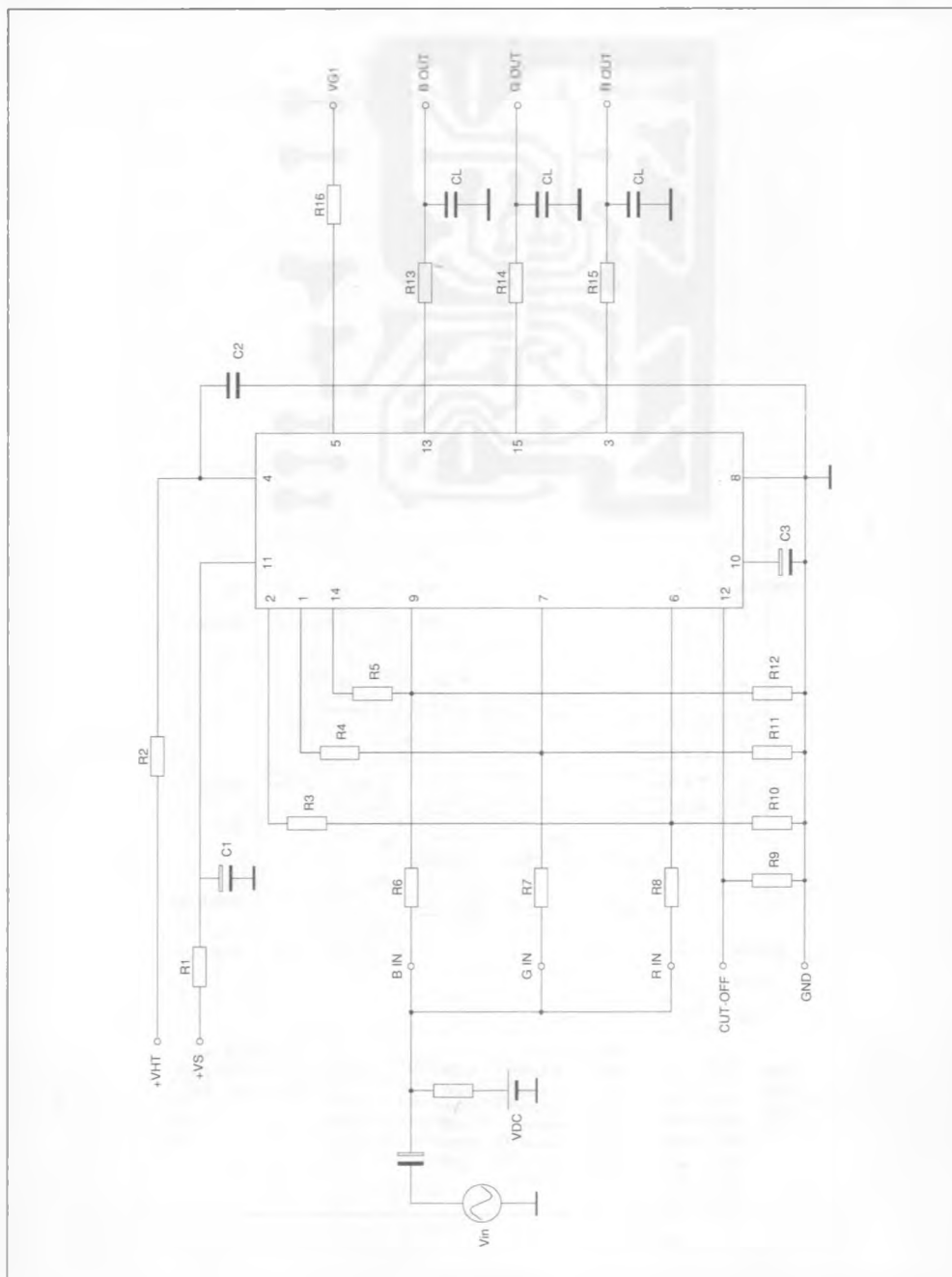
SCHEMATIC DIAGRAM



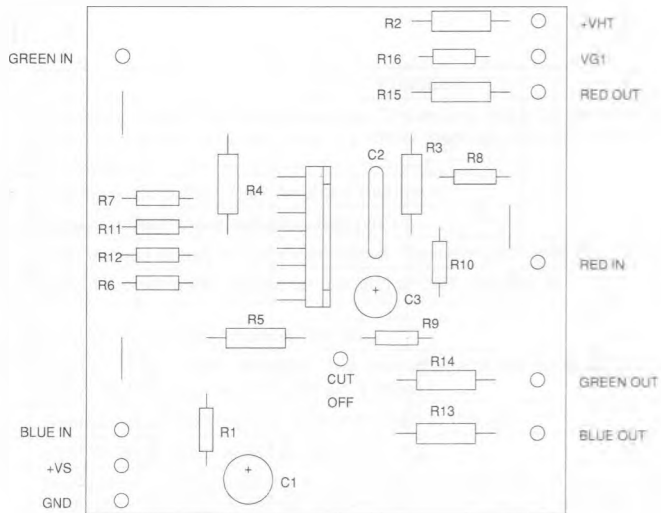
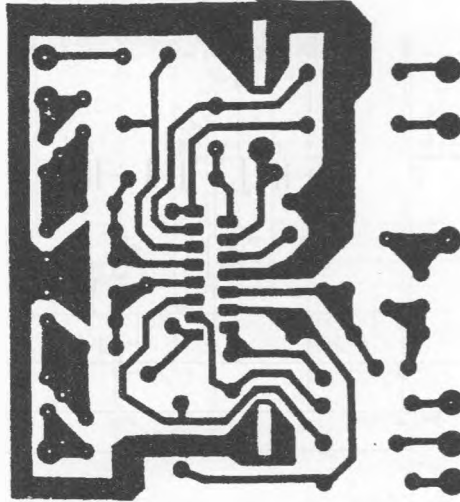
PIN FUNCTIONS

| N° | Name | Function |
|----|--------------------|--|
| 1 | GREEN FEEDBACK | Feedback Output for 'Green' Amplifier. The optimal value for the resistor connected here to set gain 68KΩ as shown in fig. 1.2. Other feedback connections are pin 2 (red) and pin 14 (blue). |
| 2 | RED FEEDBACK | Feedback Output for 'Red' Amplifier. See pin 1. |
| 3 | RED OUTPUT | Output of 'Red' Video Amplifier. See pin 15. |
| 4 | V _H | High Voltage Supply for Amplifier Stages, Typically 200V (see fig. 1.2). |
| 5 | FIRST GRID VOLTAGE | Output providing DC voltage for first grid of CRT, typically V _S + V _{BE} . |
| 6 | RED INPUT | Input of 'Red' Video Amplifier. See pin 7. |
| 7 | GREEN INPUT | Input of "Green" Video Amplifier. The bias voltage at the inputs is equal to V _{ref} + 2V _{BE} . Other inputs are pin 6 (red) and pin 9 (blue). |
| 8 | GROUND | Ground Connection (pin 8 is also connected to the tab). |
| 9 | BLUE INPUT | Input of 'Blue' Video Amplifier. See pin 7. |
| 10 | V _{ref} | The reference voltage for the three amplifiers is available on this pin. Typical value is 1.6V. The capacitor connected between pin 10 and ground eliminates AC crosstalk between the amplifiers. |
| 11 | V _S | Supply Voltage Input for Low Voltage Circuitry, typically 12V. |
| 12 | SAMPLING | Cathode Current Sampling Output. Provides sum of cathode currents for automatic cut-off adjustment with video processors using the sequential system. The three current generators I ₁ , I ₂ and I ₃ bias the inputs of this circuit which performs the cut-off adjustment, allowing adjustment also with inflowing CRT leakages. |
| 13 | BLUE OUTPUT | Output of 'Blue' Video Amplifier. See pin 15. |
| 14 | BLUE FEEDBACK | Feedback Output for 'Blue' Amplifier. See pin 1. |
| 15 | GREEN OUTPUT | Output of the 'Green' Video Amplifier. The output is protected against CRT flashovers. Other outputs are pin 3 (red) and pin 13 (blue). |

Figure 1 : Test Circuit.



TEST CIRCUIT

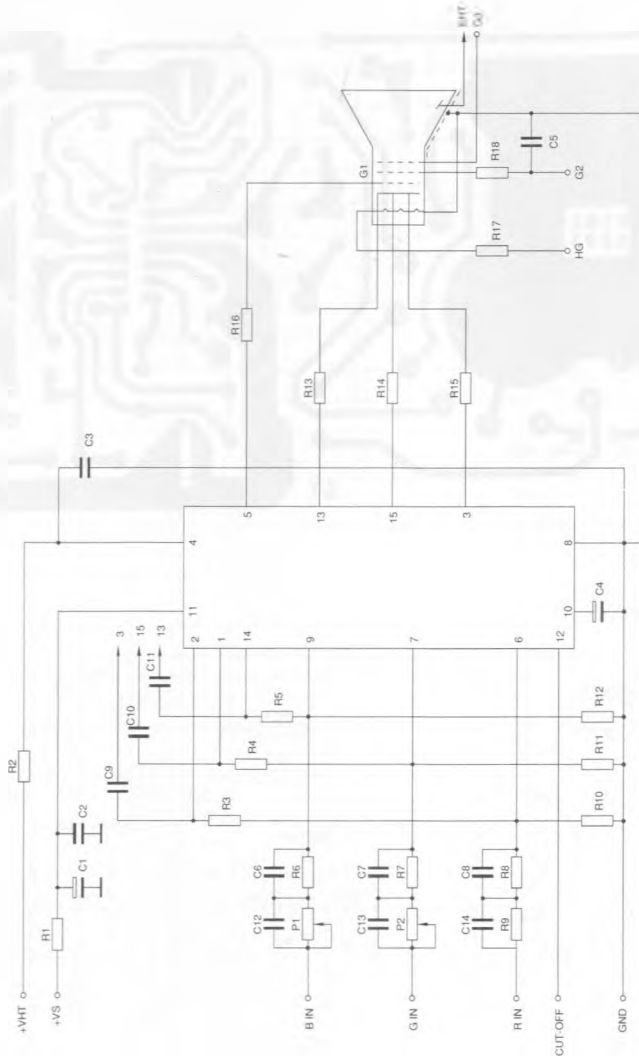


R1 = 10 ohm
 R2 = 220 ohm 1/2W
 R3 = 68 Kohm 1/2W
 R4 = 68 Kohm 1/2W
 R5 = 68 Kohm 1/2W
 R6 = 3 Kohm
 R7 = 3 Kohm
 R8 = 3 Kohm

R9 = 47 Kohm
 R10 = 1.8 Kohm 1/2W
 R11 = 1.8 Kohm 1/2W
 R12 = 1.8 Kohm 1/2W
 R13 = 1 Kohm 1/2W
 R14 = 1 Kohm 1/2W
 R15 = 1 Kohm 1/2W
 R16 = 2.2 Kohm

C1 = 47 μ F 16V
 C2 = 100nF 250V
 C3 = 10 μ F 16V

Figure 2 : Application Circuit.



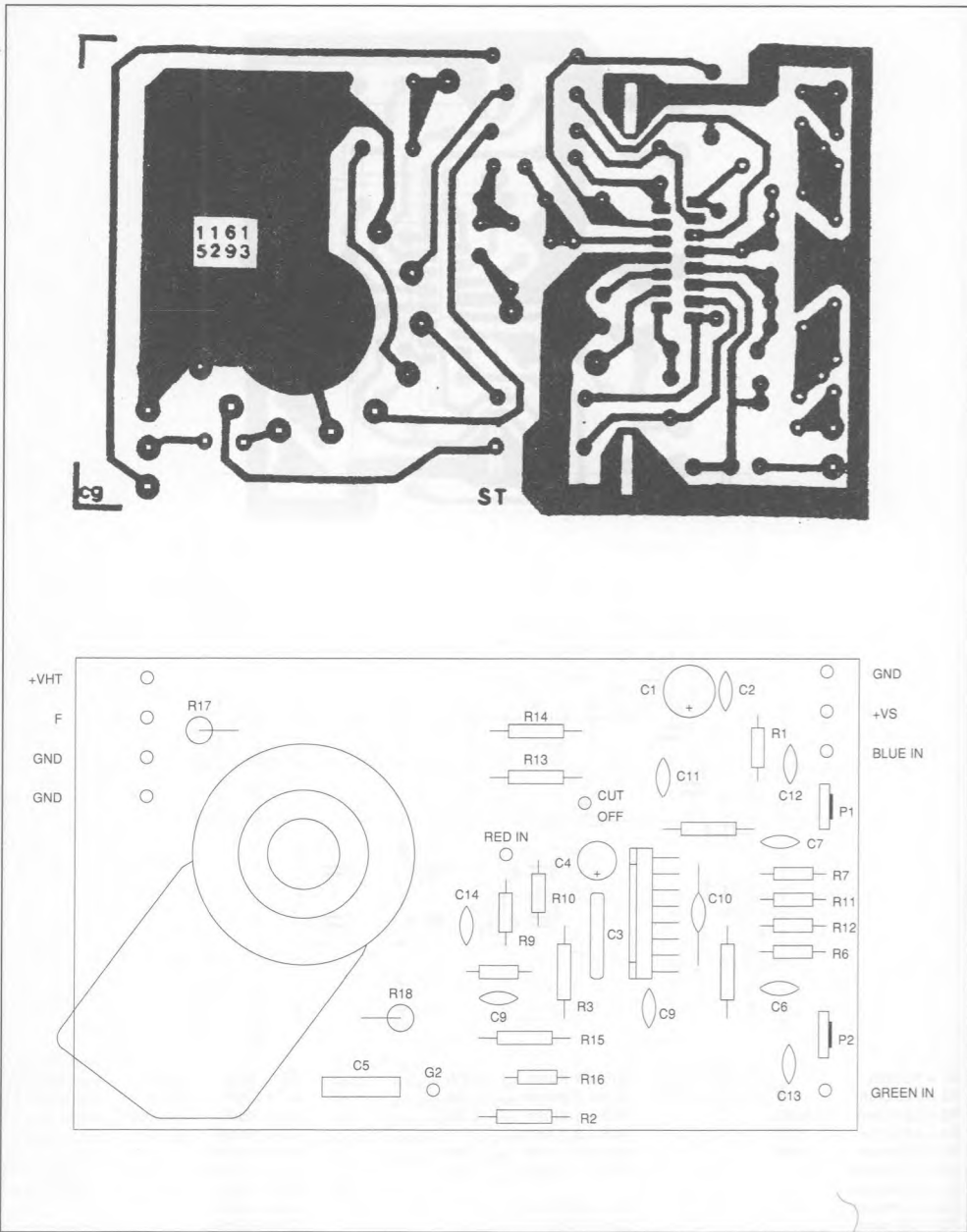
R1 = 10 ohm
 R2 = 220 ohm 1W
 R3 = 68 Kohm 1/2W
 R4 = 68 Kohm 1/2W
 R5 = 68 Kohm 1/2W
 R6 = 2.2 Kohm
 R7 = 2.2 Kohm
 R8 = 2.2 Kohm
 R9 = 680 ohm
 R10 = 1.8 Kohm
 R11 = 1.8 Kohm
 R12 = 1.8 Kohm

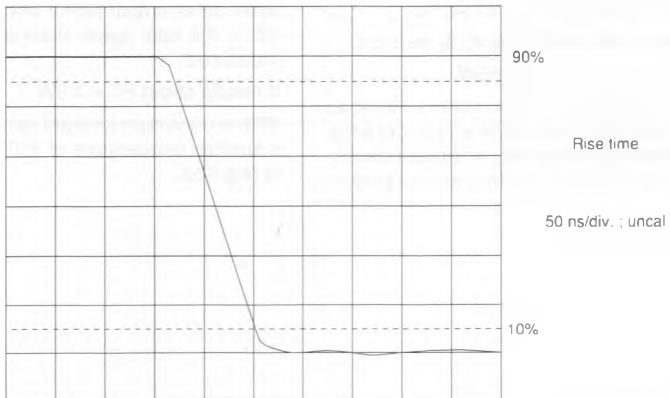
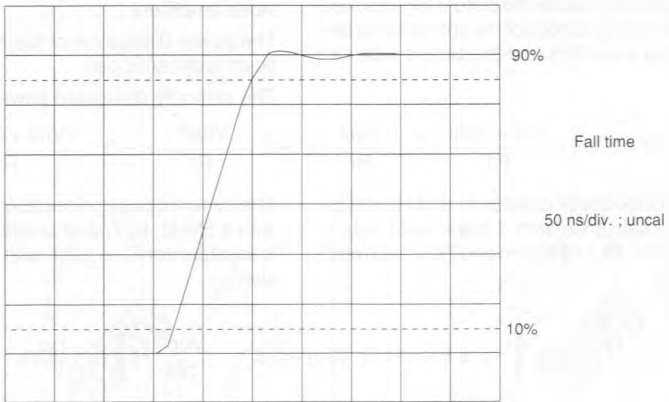
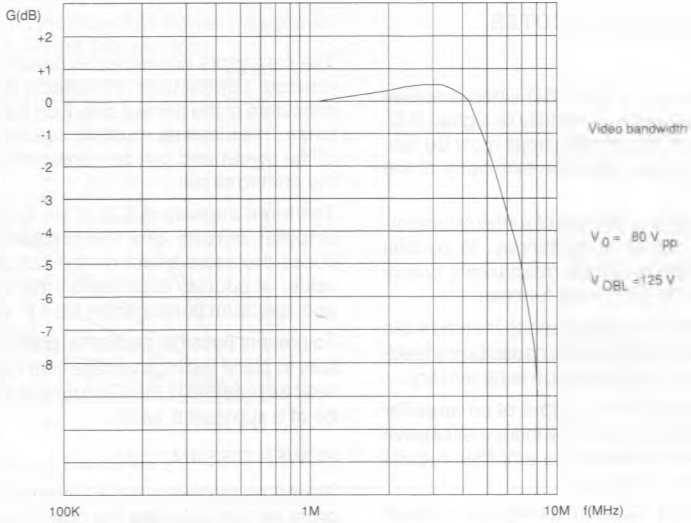
R13 = 1 Kohm 1/2W
 R14 = 1 Kohm 1/2W
 R15 = 1 Kohm 1/2W
 R16 = 2.2 Kohm
 R17 = 0.1 ohm 2W
 R18 = 10 Kohm 2W
 C1 = 47 μ F 16V
 C2 = 0.1 μ F
 C3 = 0.1 μ F 250V
 C4 = 10 μ F 16V
 C5 = 2.2nF 1KV

C6 = 22pF
 C7 = 22pF
 C8 = 22pF
 C9 = 100pF
 C10 = 100pF
 C11 = 100pF
 C12 = 68pF
 C13 = 39pF
 C14 = 68pF

P1 = 2.2 Kohm pot. lin.
 P2 = 2.2 Kohm pot. lin.

APPLICATION CIRCUIT





TDA8153 - APPLICATION NOTES

P. C. BOARD

The best performance of the RGB video amplifier can be obtained only with a carefully designed P.C. board. The layout of the printed circuit must be realized to achieve the best possible symmetry of the three channels.

Output to input capacitances are of particular importance. The input-output capacitances, in parallel with the relative high feedback resistances, create poles in the closed loop transfer function.

To optimize the band response and to minimize the channels crosstalk a low parasitic capacitance feedback resistors of not inductive type is necessary.

Capacitive coupling from the output of an amplifier and the input of another one may induce excessive crosstalk. It advisable to keep the amplifier outputs away from amplifier inputs.

The small size of the P.C. board allows you to mount the TDA8153 directly beside the picture tube socket, to minimize the capacitances of the connections between the video amplifiers and the picture tube cathodes.

$$PS = 3 V_{ht} \left(\frac{V_{ht} - V_{obl}}{R_1} + \frac{V_{obl}}{R_f} \right) - 3 \frac{V_{obl}^2}{R_f} - 3 \frac{(V_{ref} + 2V_{be})^2}{R_b}$$

Where R_f is the feedback resistance and R_b the input to ground resistance with a black level $V_{obl} = 150V$, $V_{ht} = 200V$, $R_f = 68 \text{ Kohm}$ and $R_b = 1.8 \text{ Kohm}$ we have :

$$P_s = 1.75W$$

$$P_d = 3 \left[0.8 V_{ht} (2 f C_L V_{op}) - 0.8 \frac{V_{op}^2}{2R_f} \right] = 1.90W$$

The value is reduced by 20% (0.8 factor) because during the flyback time there is not signal.

The total power dissipated by the IC is therefore :

$$PT = P_s + P_d = 1.75 + 1.90 = 3.65W$$

One of the worst working condition of the TV set as regards the power dissipation, is when you get white noise on the screen, for example, when you disconnected the TV aerial or the channels are not properly tuned.

The capacitors connected in parallel with the input resistors compensate the effects of the distribute constants of the printed circuit on the step response times. Their values must be selected on the basis of the layout and can be considered as function of the printed circuit.

The three capacitors (C_9 , C_{10} , C_{11}) between the amplifier outputs and the feedback resistors reduces the noise effect on the cut-off control, their value, of course, depends on the noise amplitude and spectrum coming from the I.F. video stage.

To prevent possible oscillation problems, it is necessary to place the high voltage filter capacitor (C_3) as near as possible to the IC ground and the latter must be of a substantial width.

POWER DISSIPATION

Taking as reference the IC internal schematic diagram we can calculate the power dissipated by the video amplifiers.

The power dissipation of the IC is defined by a static and a dynamic part.

The statically dissipated power is given by :

The dynamic power dissipation has been calculated with a 5MHz, 80Vpp sinusoidal output signal and a load capacitor $C_L = 10pF$ with the following expression :

In these cases if we set the TV receiver for 80Vpp white noise output signal with a black level $V_{obl} = 125V$, the total power dissipated by the IC can be measured.

It results about $PT = 4.8W$.

With a maximum ambient temperature of $70^\circ C$ and a junction temperature of $150^\circ C$ a $15^\circ C/W$ heatsink is required.

Figure 3 : Maximum Allowable Power Dissipation vs. Ambient Temperature.

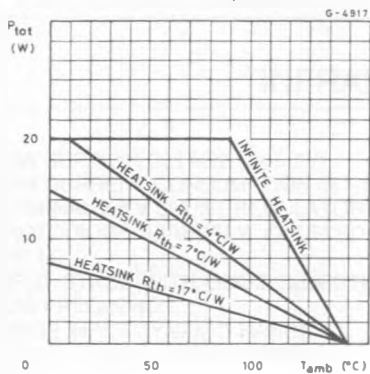


Figure 4 : Mounting Examples.

