\title{



## Features

- Self-oscillation type chopper regulator power IC using Sanyo's original IMST (Insulated Metal Substrate Technology) substrate.
- The STK795, being a 5V chopper IC, is more advantageous in the following points as compared with series regulator (dropper type) ICs.

1. Possible to provide a 5 V output power supply circuit with high efficiency.
2. Since the input voltage range is wide, no more than one rectifying/smoothing circuit is required to provide a multi-output power supply circuit which also delivers 12 V or 24 V output.

- Functional trimming is used to set 5 V output with high accuracy.
- Cutoff function to cut off output voltage by external signal.
- Contains a transistor for overcurrent protector (foldback characteristic) and possibel to set the protection level externally.


## Package Dimensions

unit:mm
4063A


## Specifications

Maximum Ratings at $\mathrm{Ta}=25^{\circ} \mathrm{C}$

| Parameter | Symbol | Conditions | Ratings | Unit |
| :--- | :---: | :---: | :---: | :---: |
| Maximum DC Input Voltage | $\mathrm{V}_{\text {in }(\mathrm{DC})} \max$ |  | 40 | V |
| Maximum Output Current | $\mathrm{I}_{\mathrm{O}} \mathrm{max}$ |  | 3 | A |
| Operating Substrate Temperature | Tc |  | ${ }^{\circ} \mathrm{C}$ |  |
| Junction Temperature | Tj |  | ${ }^{\circ} \mathrm{C}$ |  |
| Storage temperature | Tstg |  | ${ }^{\circ} \mathrm{C}$ |  |

Operating Characteristics at $\mathrm{Ta}=25^{\circ} \mathrm{C}$, See specified Test Circuit.

| Parameter | Symbol | Conditions | Ratings |  |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | min | typ | max |  |
| Output Voltage | $\mathrm{V}_{\mathrm{O}}$ | $\mathrm{V}_{\mathrm{IN}}=12 \mathrm{~V}, \mathrm{I}_{\mathrm{O}}=1.5 \mathrm{~A}$ | 4.9 | 5.0 | 5.1 | V |
| Line Regulation |  | $\mathrm{V}_{\mathrm{IN}}=10$ to $15 \mathrm{~V}, \mathrm{I}_{\mathrm{O}}=1.5 \mathrm{~A}$ |  | 70 | 100 | mV |
| Load Regulation |  | $\mathrm{V}_{\text {IN }}=12 \mathrm{~V}, \mathrm{I}_{\mathrm{O}}=0.5$ to 3 A |  | 30 | 60 | mV |
| Efficiency |  | $\mathrm{V}_{\mathrm{IN}}=12 \mathrm{~V}, \mathrm{l}_{\mathrm{O}}=1.5 \mathrm{~A}$ |  | 72 |  | \% |
| Frequency | f | $\mathrm{V}_{\mathrm{IN}}=12 \mathrm{~V}, \mathrm{I}_{\mathrm{O}}=1.5 \mathrm{~A}$ |  | 35 |  | kHz |
| Temperature Coefficient |  | $\mathrm{V}_{\mathrm{IN}}=12 \mathrm{~V}, \mathrm{l}_{\mathrm{O}}=1.5 \mathrm{~A}$ |  | 1 |  | $\mathrm{mV} /{ }^{\circ} \mathrm{C}$ |

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## Equivalent Circuit



## Test Circuit



Unit (resistance: $\Omega$, capacitance: F)
Note) • D1 : Schottky barrier diode SB40-05.
B. C. : Beads core, 2 to $3 \mu \mathrm{H}$.

C3, B. C. are used to reduce switching spike noise.
TR1 is used to provide overcurrent protection.
If no protection is required, remove TR1.
A current of 0.5 A min. must flow in the load.








## Thermal Design

The total internal power dissipation in the IC is related to the output current as shown below. Assuming $\mathrm{V}_{\mathrm{in}(\mathrm{DC})}=12 \mathrm{~V}$, output current $=3 \mathrm{~A}$, the total internal power dissipation is 2.7 W .


Assuming that the IC case temperature ( Al plate) is $85^{\circ} \mathrm{C}\left(\mathrm{Tc} \max =105^{\circ} \mathrm{C}\right.$ ) and the temperature inside equipment is $60^{\circ} \mathrm{C}$ max., the thermal resistance required of the heat sink is as shown below.

$$
\begin{aligned}
\theta \mathrm{c}-\mathrm{a} & =\frac{85^{\circ} \mathrm{C}-60^{\circ} \mathrm{C}}{2.7 \mathrm{~W}} \\
& =9.3^{\circ} \mathrm{C} / \mathrm{W}
\end{aligned}
$$



For 2 mm thick Al plate (black coated), the area is $30 \mathrm{~cm}^{2}$. ( $55 \times 55 \times 2 \mathrm{t}$ )
Junction temperature Tj of the power transistor which forms a main heat source is calculated as follows :
The thermal resistance of the power transistor is : $\theta \mathrm{j}-\mathrm{c}=6.2^{\circ} \mathrm{C} / \mathrm{W}$
Therefore, Tj is calculated using $\mathrm{Tj}=\mathrm{Pd} \times \theta \mathrm{j}-\mathrm{c}+\mathrm{Tc}$.

$$
\mathrm{Tj}=2.7 \mathrm{~W} \times 6.2^{\circ} \mathrm{C} / \mathrm{W}+85^{\circ} \mathrm{C}=101.7^{\circ} \mathrm{C}
$$

Since the actual thermal resistance of the heat sink greatly depends on various conditions such as the layout of equipment or ventilation, allow an ample margin in thermal design.
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