SP8000 SERIES HIGH SPEED DIVIDERS

SP8670 A, B&M 600MHz÷8 SP8671 A, B&M 500MHz÷8 SP8672 A, B&M 400MHz÷8

The SP8670, SP8671 and SP8672 are fixed ratio -8 asynchronous ECL counters with a maximum operating frequency of 600, 500 and 400 MHz respectively. The operating temperature is specified by the final coding letter: $-55^{\circ}C$ to $+125^{\circ}C$ ('A' grade), 0°C to $+70^{\circ}C$ ('B' grade) and -40°C to +85°C ('M' grade). The input is normally capacitively coupled to the signal source but the circuit can be DC driven if required. The inputs can be either single driven, relative to the on-chip reference voltage, or driven differentially. There are two complementary emitter-follower outputs.

Low Power - Typically 250mW

ECL II & ECL III Output Compatibility

Easy Operation From UHF Signal Source

0/P 01	14 V10	
NC	D NC	
NC	VREF	
0/P	D NC	
NC	þ	
NC] NC	
-VE VEE] 1/P	DC14 DG14

Fig. 1 Pin connections

APPLICATIONS

- Prescaling for UHF Synthesisers
- 1 Instrumentation

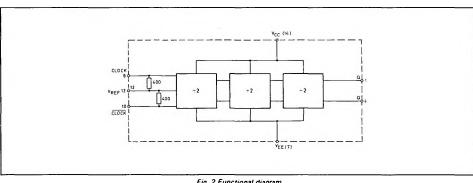


Fig. 2 Functional diagram

QUICK REFERENCE DATA

FEATURES

55

25

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ŝ	Power Supplies	V _{CC} = 0V V _{FF} = -5.2V ± 0.25V
14	Input Amplitude range	400mV to 800mV p-p
30	Output Voltage Swing	800mV typ. p-p
8-92 - 2	Temp. Ranges:	-55°C to +125°C ('A' Grade) 0°C to +70°C ('B' Grade) -40°C to +85°C ('M' Grade)



SEMICONDUCTORS

SP8670

ELECTRICAL CHARACTERISTICS

Test Conditions (unless otherwise stated)

T _{amb} ≂	'A' grade: -55°C to +125°C;
Supply Voltage	'B' grade: 0°C to 70°C;
V _{CC} = 0V	'M' grade: -40°C to +85°C;
V _{EE} = -5.2V ± 0.25V	
Output load = 500Ω line in	parallel with approx. 3pF

Characteristic		Value		Units	Condition	
		Min.	Min. Typ. Max.	Units	Condition	
Max. Toggle frequency	SP8670 SP8671 SP8672	600 500 400			MHz MHz MHz	Test circuit as in fig. 2 V _{IN} = 400 to 800mV p·p
Min, Toggle frequency for correct operation with a sinewave input				40	MHz	V _{IN} = 400 to 800mV p-p
Min. slew rate for square wave input to guarantee correct operation to OHz			26	100	V/µs V	
Input reference voltage Output voltage swing (dynamic)		500	2.6 800		w mV	p-p
Output voltage (static) High state		-8.95		.615	v	
Low state Power supply drain current		-1.83	45	-1.435 60	V mA	

Toggle Frequency Test Circuit

- All leads are kept short to minimise stray capacitance and inductance
- 2. Resistors and capacitors are non-inductive UHF types.
- 3. Device is tested in a 14 lead Augat socket type No. 314-AGGA-R

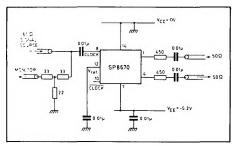


Fig. 3 Toggle frequency test circuit

OPERATING NOTE

Normal UHF layout techniques should be used to ensure satisfactory operation. If the positive supply is used as the earth connection, noise immunity is improved and the risk of damage due to inadvertently shorting the output emitter followers to the negative rail is reduced. The circuit is normally capacitively coupled to the signal source. In the absence of an input signal the circuit will self-oscillate. This can be prevented by connecting a $10K\Omega$ resistor between one of the inputs and the negative rail.

 V_{re1} must be decoupled to RF earth by a capacitor in the range 30pF to 1000pF. It is important that this decoupling is adequate, otherwise input sensitivity will be reduced.

The device will also miscount if the input transitions are slow -a slew rate of $100V/\mu s$ or greater is necessary for low frequency operation.

The outputs interface directly to ECL II or to ECL 10K with a potential divider (see Fig. 4).

A typical application of the SL8670 would be in the divider chain of a synthesiser operating in the military frequency range 225 MHz to 512 MHz. A binary division ratio is optimum where power is at a premium and so the SP8670 series would normally be used in low power applications.

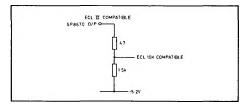


Fig. 4 SP8670 to ECL 10K interface

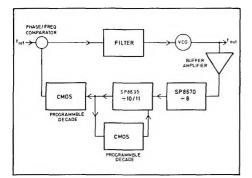


Fig. 5 A low power synthesiser loop

ABSOLUTE MAXIMUM RATINGS

Power supply volt	8 volts	
Input voltage	VINac	2.5V p-p
Output source cur	tûmA	
Storage temperatu	-55°C to +125°C	
Operating junction	150°C max.	