## FEATURES

- Qualified for Automotive Applications
- 2-V to $5.5-\mathrm{V} \mathrm{V}_{\mathrm{cc}}$ Operation
- Unbuffered Outputs
- Typical $\mathrm{V}_{\text {olp }}$ (Output Ground Bounce) $<0.8 \mathrm{~V}$ at $\mathrm{V}_{\mathrm{CC}}=3.3 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$
- Typical $\mathrm{V}_{\text {OHV }}$ (Output $\mathrm{V}_{\text {OH }}$ Undershoot) $>2.3 \mathrm{~V}$ at $\mathrm{V}_{\mathrm{CC}}=3.3 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$
- Supports Mixed-Mode Voltage Operation on All Ports



## DESCRIPTION/ORDERING INFORMATION

This hex inverter is designed for $2-\mathrm{V}$ to $5.5-\mathrm{V} \mathrm{V}_{\mathrm{CC}}$ operation.
The SN74LVU04A-Q1 contains six independent inverters with unbuffered outputs. This device performs the Boolean function $Y=\bar{A}$.

ORDERING INFORMATION ${ }^{(1)}$

| $\mathbf{T}_{\mathbf{A}}$ | PACKAGE ${ }^{(2)}$ |  | ORDERABLE PART NUMBER | TOP-SIDE MARKING |
| :---: | :--- | :--- | :--- | :--- |
| $-40^{\circ} \mathrm{C}$ to $125^{\circ} \mathrm{C}$ | TSSOP - PW | Reel of 2000 | SN74LVU04AQPWRQ1 | LU04AQ |

(1) For the most current package and ordering information, see the Package Option Addendum at the end of this document, or see the TI web site at www.ti.com
(2) Package drawings, thermal data, and symbolization are available at www.ti.com/packaging.

| FUNCTION TABLE <br> (EACH INVERTER) |
| :---: |
| INPUT |
| A |
| H |
| LY Y |

## LOGIC DIAGRAM, EACH INVERTER (POSITIVE LOGIC)

A


## Absolute Maximum Ratings ${ }^{(1)}$

over operating free-air temperature range (unless otherwise noted)

|  |  |  | MIN | MAX | UNIT |
| :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathrm{V}_{\mathrm{CC}}$ | Supply voltage range |  | -0.5 | 7 | V |
| $\mathrm{V}_{1}$ | Input voltage range ${ }^{(2)}$ |  | -0.5 | 7 | V |
| $\mathrm{V}_{\mathrm{O}}$ | Output voltage range ${ }^{(2)(3)}$ |  | -0.5 | $\mathrm{V}_{C C}+0.5$ | V |
| $\mathrm{I}_{\mathrm{IK}}$ | Input clamp current | $\mathrm{V}_{1}<0$ |  | -20 | mA |
| Iok | Output clamp current | $\mathrm{V}_{\mathrm{O}}<0$ |  | -50 | mA |
| $\mathrm{l}_{0}$ | Continuous output current | $\mathrm{V}_{\mathrm{O}}=0$ to $\mathrm{V}_{\mathrm{CC}}$ |  | $\pm 25$ | mA |
|  | Continuous current through $\mathrm{V}_{\text {CC }}$ or GND |  |  | $\pm 50$ | mA |
| $\theta_{\text {JA }}$ | Package thermal impedance ${ }^{(4)}$ |  |  | 113 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
|  | ESD rating ${ }^{(5)}$ | Human-Body Model |  | 1.5 (H1C) | kV |
|  |  | Charged-Device Model |  | 1 (C5) |  |
|  |  | Machine Model |  | 200 (M3) | V |
| $\mathrm{T}_{\text {stg }}$ | Storage temperature range |  | -65 | 150 | ${ }^{\circ} \mathrm{C}$ |

(1) Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
(2) The input and output voltage ratings may be exceeded if the input and output current ratings are observed.
(3) This value is limited to 5.5 V maximum.
(4) The package thermal impedance is calculated in accordance with JESD 51-7.
(5) ESD protection level per AEC Q100 classification

Recommended Operating Conditions ${ }^{(1)}$

(1) All unused inputs of the device must be held at $\mathrm{V}_{\mathrm{CC}}$ or GND to ensure proper device operation. Refer to the TI application report, Implications of Slow or Floating CMOS Inputs, literature number SCBA004.

InSTRUMENTS

## Electrical Characteristics

over recommended operating free-air temperature range (unless otherwise noted)

| PARAMETER | TEST CONDITIONS |  | $\mathrm{V}_{\mathrm{cc}}$ | $-40^{\circ} \mathrm{C}$ to $125^{\circ} \mathrm{C}$ |  |  | $-40^{\circ} \mathrm{C}$ to $85^{\circ} \mathrm{C}$ |  |  | UNIT |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | MIN | TYP | MAX | MIN | TYP | MAX |  |
| $\mathrm{V}_{\mathrm{OH}}$ | $\mathrm{I}_{\mathrm{OH}}=-50 \mu \mathrm{~A}$ | $\mathrm{V}_{\mathrm{LL}}=0 \mathrm{~V}$ |  | 2 V to 5.5 V | $\mathrm{V}_{C C}-0.1$ |  |  | $\mathrm{V}_{C C}-0.1$ |  |  | V |
|  | $\mathrm{I}_{\mathrm{OH}}=-2 \mathrm{~mA}$ |  | 2.3 V | 2 |  |  | 2 |  |  |  |  |
|  | $\mathrm{I}_{\mathrm{OH}}=-6 \mathrm{~mA}$ |  | 3 V | 2.48 |  |  | 2.48 |  |  |  |  |
|  | $\mathrm{l}_{\mathrm{OH}}=-12 \mathrm{~mA}$ |  | 4.5 V | 3.7 |  |  | 3.8 |  |  |  |  |
| $\mathrm{V}_{\text {OL }}$ | $\mathrm{IOL}^{\prime}=50 \mu \mathrm{~A}$ | $\mathrm{V}_{\mathrm{IH}}=\mathrm{V}_{\mathrm{CC}}$ | 2 V to 5.5 V |  |  | 0.1 |  |  | 0.1 | V |  |
|  | $\mathrm{I}_{\mathrm{OL}}=2 \mathrm{~mA}$ |  | 2.3 V |  |  | 0.4 |  |  | 0.4 |  |  |
|  | $\mathrm{I}_{\mathrm{OL}}=6 \mathrm{~mA}$ |  | 3 V |  |  | 0.44 |  |  | 0.44 |  |  |
|  | $\mathrm{I}_{\mathrm{LL}}=12 \mathrm{~mA}$ |  | 4.5 V |  |  | 0.55 |  |  | 0.55 |  |  |
| 1 | $\mathrm{V}_{1}=5.5 \mathrm{~V}$ or GND |  | 0 V to 5.5 V |  |  | $\pm 1$ |  |  | $\pm 1$ | $\mu \mathrm{A}$ |  |
| $\mathrm{I}_{\mathrm{CC}}$ | $\mathrm{V}_{\mathrm{I}}=\mathrm{V}_{\mathrm{CC}}$ or GND, $\mathrm{l}_{\mathrm{O}}=0$ |  | 5.5 V |  |  | 20 |  |  | 20 | $\mu \mathrm{A}$ |  |
| $\mathrm{C}_{i}$ | $\mathrm{V}_{\mathrm{I}}=\mathrm{V}_{\mathrm{CC}}$ or GND |  | 3.3 V | 4 |  |  | 4 |  |  | pF |  |

## Switching Characteristics

over recommended operating free-air temperature range, $\mathrm{V}_{\mathrm{CC}}=3.3 \mathrm{~V} \pm 0.3 \mathrm{~V}$ (unless otherwise noted) (see Figure 1)

| PARAMETER | FROM(INPUT) | $\begin{gathered} \text { TO } \\ \text { (OUTPUT) } \end{gathered}$ | LOAD CAPACITANCE | $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ |  |  | $-40^{\circ} \mathrm{C}$ to $125^{\circ} \mathrm{C}$ |  | $-40^{\circ} \mathrm{C}$ to $85^{\circ} \mathrm{C}$ |  | UNIT |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  | MIN | TYP | MAX | MIN | MAX | MIN | MAX |  |
| $\mathrm{t}_{\mathrm{pd}}$ | A | Y | $\mathrm{C}_{\mathrm{L}}=50 \mathrm{pF}$ |  | 4.7 | 11.4 | 1 | 16 | 1 | 13 | ns |

## Switching Characteristics

over recommended operating free-air temperature range, $\mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V} \pm 0.5 \mathrm{~V}$ (unless otherwise noted) (see Figure 1)

| PARAMETER | FROM (INPUT) | TO (OUTPUT) | LOAD CAPACITANCE | $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ |  |  | $-40^{\circ} \mathrm{C}$ to $125^{\circ} \mathrm{C}$ |  | $-40^{\circ} \mathrm{C}$ to $85^{\circ} \mathrm{C}$ |  | UNIT |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  | MIN | TYP | MAX | MIN | MAX | MIN | MAX |  |
| $\mathrm{t}_{\mathrm{pd}}$ | A | Y | $\mathrm{C}_{\mathrm{L}}=50 \mathrm{pF}$ |  | 3.9 | 7 | 1 | 11 | 1 | 8 | ns |

## Noise Characteristics

$\mathrm{V}_{\mathrm{CC}}=3.3 \mathrm{~V}, \mathrm{C}_{\mathrm{L}}=50 \mathrm{pF}, \mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}^{(1)}$

|  | PARAMETER | MIN |
| :--- | :---: | :---: |
| $\mathrm{V}_{\mathrm{OL}(\mathrm{P})}$ | Quiet output, maximum dynamic $\mathrm{V}_{\mathrm{OL}}$ | TYP |
| $\mathrm{V}_{\mathrm{OL}(\mathrm{V})}$ | Quiet output, minimum dynamic $\mathrm{V}_{\mathrm{OL}}$ | MAX |
| UNIT |  |  |
| $\mathrm{V}_{\mathrm{OH}(\mathrm{V})}$ | Quiet output, minimum dynamic $\mathrm{V}_{\mathrm{OH}}$ | 0.5 |
| $\mathrm{~V}_{\mathrm{IH}(\mathrm{D})}$ | High-level dynamic input voltage | 0.8 |
| $\mathrm{~V}_{\mathrm{IL}(\mathrm{D})}$ | Low-level dynamic input voltage | V |

(1) Characteristics are for surface-mount packages only.

## Operating Characteristics

$\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$

| PARAMETER | TEST CONDITIONS | $\mathbf{V}_{\mathbf{c c}}$ | TYP | UNIT |
| :---: | :---: | :---: | :---: | :---: |
| $\mathrm{C}_{\mathrm{pd}}$ | Power dissipation capacitance | $\mathrm{C}_{\mathrm{L}}=50 \mathrm{pF}, \mathrm{f}=10 \mathrm{MHz}$ | 3.3 V | 5.6 |
|  |  | 5 pF |  |  |

## PARAMETER MEASUREMENT INFORMATION



Figure 1. Load Circuits and Voltage Waveforms

INSTRUMENTS

## PACKAGING INFORMATION

| Orderable Device | Status <br> (1) | Package Type | Package Drawing | Pins | Package Qty | Eco Plan <br> (2) | Lead/Ball Finish | MSL Peak Temp <br> (3) | Samples <br> (Requires Login) |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| SN74LVU04AQPWRG4Q1 | ACTIVE | TSSOP | PW | 14 | 2000 | Green (RoHS \& no Sb/Br) | CU NIPDAU | Level-1-260C-UNLIM |  |
| SN74LVU04AQPWRQ1 | OBSOLETE | TSSOP | PW | 14 |  | TBD | Call TI | Call TI |  |

${ }^{(1)}$ The marketing status values are defined as follows:
ACTIVE: Product device recommended for new designs.
LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.
NRND: Not recommended for new designs. Device is in production to support existing customers, but Tl does not recommend using this part in a new design.
PREVIEW: Device has been announced but is not in production. Samples may or may not be available.
OBSOLETE: TI has discontinued the production of the device.
${ }^{(2)}$ Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS \& no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.
TBD: The Pb-Free/Green conversion plan has not been defined.
Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed $0.1 \%$ by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.
Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb -Free (RoHS compatible) as defined above.
Green (RoHS \& no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed $0.1 \%$ by weight in homogeneous material)
${ }^{(3)}$ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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OTHER QUALIFIED VERSIONS OF SN74LVU04A-Q1:

- Catalog: SN74LVU04A

NOTE: Qualified Version Definitions:

- Catalog - TI's standard catalog product


NOTES: A. All linear dimensions are in millimeters. Dimensioning and tolerancing per ASME Y14.5M-1994.
B. This drawing is subject to change without notice.

Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0,15 each side.
(D) Body width does not include interlead flash. Interlead flash shall not exceed 0,25 each side.
E. Falls within JEDEC MO-153

## PW (R-PDSO-G14)

## PLASTIC SMALL OUTLINE



NOTES: A. All linear dimensions are in millimeters.
B. This drawing is subject to change without notice.
C. Publication IPC-7351 is recommended for alternate designs.
D. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Refer to IPC-7525 for other stencil recommendations.
E. Customers should contact their board fabrication site for solder mask tolerances between and around signal pads.

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