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- 3-State Versions of SN54F153 and SN74F153
- Permits Multiplexing From N Lines to One Line
- Performs Parallel-to-Serial Conversion
- Package Options Include Plastic Small-Outline Packages, Ceramic Chip Carriers, and Standard Plastic and Ceramic 300-mil DIPs

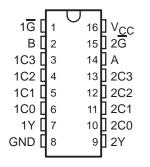
### description

These data selectors/multiplexers contain inverters and drivers to supply full binary decoding data selection to the AND-OR gates. Separate output-control inputs are provided for each of the two 4-line sections.

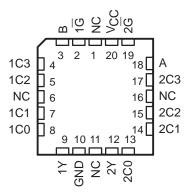
The 3-state outputs can interface with and drive data lines of bus-organized systems. With all but one of the common outputs disabled (at a high-impedance state), the low impedance of the single enabled output will drive the bus line to a high or low logic level. Each output has its own strobe  $(\overline{G})$  inputs. The output is disabled when its strobe is high.

The SN54F253 is characterized for operation over the full military temperature range of  $-55^{\circ}$ C to 125°C. The SN74F253 is characterized for operation from 0°C to 70°C.

#### SN54F253 . . . J PACKAGE SN74F253 . . . D OR N PACKAGE (TOP VIEW)



## SN54F253 . . . FK PACKAGE (TOP VIEW)



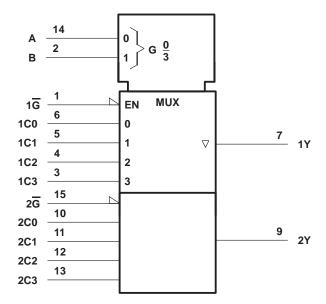
NC - No internal connection

#### **FUNCTION TABLE**

		OTDODE	OUTDUT				
SEL	ECT		DA	TA	STROBE G	OUTPUT	
В	Α	C0	C0 C1 C2 C3		,	·	
Х	X	Χ	X	Χ	X	Н	Z
L	L	L	X	X	X	L	L
L	L	Н	X	X	X	L	Н
L	Н	Χ	L	Χ	X	L	L
L	Н	Χ	Н	Χ	X	L	Н
Н	L	Χ	Χ	L	X	L	L
Н	L	Χ	Χ	Н	X	L	Н
Н	Н	Х	Χ	Χ	L	L	L
Н	Н	Х	Χ	Χ	Н	L	Н

Select inputs A and B are common to both sections.

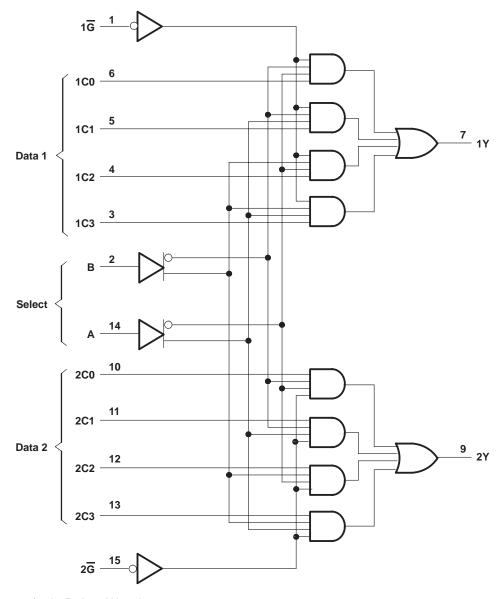
## logic symbol†



 $<sup>\</sup>ensuremath{^{\dagger}}$  This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12. Pin numbers shown are for the D, J, and N packages.



## logic diagram (positive logic)



Pin numbers shown are for the D, J, and N packages.

## SN54F253, SN74F253 DUAL 1-OF-4 DATA SELECTORS/MULTIPLEXERS WITH 3-STATE OUTPUTS

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## absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage range, V <sub>CC</sub>		0.5 V to 7 V
Input voltage range (see Note 1)		1.2 V to 7 V
Input current range		–30 mA to 5 mA
Voltage range applied to any output in	the disabled or power-off state .	0.5 V to 5.5 V
Voltage range applied to any output in	the high state	0.5 V to V <sub>CC</sub>
Current into any output in the low state	: SN54F253	40 mA
	SN74F253	48 mA
Operating free-air temperature range:	SN54F253	–55°C to 125°C
	SN74F253	0°C to 70°C
Storage temperature range		–65°C to 150°C

<sup>†</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 1: The input voltage ratings may be exceeded provided the input current ratings are observed.

### recommended operating conditions

		S	SN54F253			SN74F253			
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT	
VCC	Supply voltage	4.5	5	5.5	4.5	5	5.5	V	
VIH	High-level input voltage	2			2			V	
VIL	Low-level input voltage			8.0			0.8	V	
liK	Input clamp current			-18			-18	mA	
IOH	High-level output current			-3			- 3	mA	
lOL	Low-level output current			20			24	mA	
TA	Operating free-air temperature	-55		125	0		70	°C	

## SN54F253, SN74F253 DUAL 1-OF-4 DATA SELECTORS/MULTIPLEXERS WITH 3-STATE OUTPUTS

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# electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

	TEST COMPITIONS			N54F25	3	S			
PARAMETER	TES	T CONDITIONS	MIN	TYP†	MAX	MIN	TYP†	MAX	UNIT
VIK	V <sub>CC</sub> = 4.5 V,	$I_{I} = -18 \text{ mA}$			-1.2			-1.2	V
	V 45V	I <sub>OH</sub> = – 1 mA	2.5	3.4		2.5	3.4		
VOH	$V_{CC} = 4.5 \text{ V}$	$I_{OH} = -3 \text{ mA}$	2.4	3.3		2.4	3.3		V
	$V_{CC} = 4.75 \text{ V},$	$I_{OH} = -1 \text{ mA to } -3 \text{ mA}$				2.7			
.,	V 45.V	I <sub>OL</sub> = 20 mA		0.3	0.5				V
V <sub>OL</sub>	$V_{CC} = 4.5 \text{ V}$	I <sub>OL</sub> = 24 mA					0.35	0.5	V
lozh	V <sub>CC</sub> = 5.5 V,	V <sub>O</sub> = 2.7 V			50			50	μΑ
lozL	V <sub>CC</sub> = 5.5 V,	V <sub>O</sub> = 0.5 V			-50			-50	μΑ
lj	V <sub>CC</sub> = 5.5 V,	V <sub>I</sub> = 7 V			0.1			0.1	mA
lіН	$V_{CC} = 5.5 \text{ V},$	V <sub>I</sub> = 2.7 V			20			20	μΑ
I <sub>IL</sub>	$V_{CC} = 5.5 \text{ V},$	V <sub>I</sub> = 0.5 V			- 0.6			- 0.6	mA
los <sup>‡</sup>	$V_{CC} = 5.5 \text{ V},$	V <sub>O</sub> = 0	-60		-150	-60		-150	mA
Іссн		Condition A		11.5	16		11.5	16	
ICCL	V <sub>CC</sub> = 5.5 V, See Note 2	Condition B		16	23		16	23	mA
Iccz		Condition C		16	23		16	23	

<sup>&</sup>lt;sup>†</sup> All typical values are at  $V_{CC} = 5 \text{ V}$ ,  $T_A = 25^{\circ}\text{C}$ .

NOTE 2: I<sub>CC</sub> is measured with the outputs open under the following conditions:

A. Inputs A, B, 1C3, and 2C3 at 4.5 V, other inputs grounded

B. All inputs grounded

C. Inputs 1G and 2G at 4.5 V, other inputs grounded

## switching characteristics (see Note 3)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	C <sub>l</sub> R <sup>2</sup> R2	CC = 5 V L = 50 p 1 = 500 s 2 = 500 s 4 = 25°C	F, Ω, Ω,	C R R	L = 50 p 1 = 500 Ω 2 = 500 Ω	2,		UNIT
			′F253			SN54F253		SN74F253		
			MIN	TYP	MAX	MIN	MAX	MIN	MAX	1
<sup>t</sup> PLH	A == D	A V	3.7	8.1	11.5	2.7	15	3.7	13	ns
<sup>t</sup> PHL	A or B	Any Y	2.2	6.1	9	1.7	11	2.2	10	
<sup>t</sup> PLH	A C	A V	2.2	5.1	7	1.7	9	2.2	8	
<sup>t</sup> PHL	Any C	Any Y	1.7	4.1	6	1.7	8	1.7	7	ns
<sup>t</sup> PZH	G	A V	2.2	5.6	8	1.7	10	2.2	9	
t <sub>PZL</sub>	G	Any Y	2.2	5.6	8	1.7	10	2.2	9	ns
<sup>t</sup> PHZ	G	Any	1.2	3.3	5	1.2	6.5	1.2	6	20
<sup>t</sup> PLZ	G	Any Y	1.2	4	6	1.2	8	1.2	7	ns

§ For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

NOTE 3: Load circuit and waveforms are shown in Section 1.



<sup>‡</sup> Not more than one output should be shorted at a time, and the duration of the short circuit should not exceed one second.

PACKAGE MATERIALS INFORMATION

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## TAPE AND REEL INFORMATION





_		
		Dimension designed to accommodate the component width
	B0	Dimension designed to accommodate the component length
	K0	Dimension designed to accommodate the component thickness
	W	Overall width of the carrier tape
ı	P1	Pitch between successive cavity centers

### QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



#### \*All dimensions are nominal

Device	Package Type	Package Drawing			Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
SN74F253DR	SOIC	D	16	2500	330.0	16.4	6.5	10.3	2.1	8.0	16.0	Q1

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#### \*All dimensions are nominal

Device	Package Type	Package Drawing	kage Drawing Pins		Length (mm)	Width (mm)	Height (mm)	
SN74F253DR	SOIC	D	16	2500	333.2	345.9	28.6	

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