- 100K Compatible
- Inverting Outputs
- ECL and TTL Control Inputs
- Flow-Through Architecture Optimizes PCB Layout
- Center Pin V<sub>CC</sub>, V<sub>EE</sub>, and GND Configurations Minimize High-Speed Switching Noise
- Package Options Include "Small Outline" Packages and Standard Plastic 300-mil DIPs

#### description

This octal ECL-to-TTL translator is designed to provide a efficient translation between a 100K ECL signal environment and a TTL signal environment. This device is designed specifically to improve the performance and density of ECL-to-TTL CPU/bus-oriented functions such as memory-address drivers, clock drivers, and bus-oriented receivers and transmitters.

Two output pins,  $\overline{OE1}$  and  $\overline{OE2}$ , are provided. These control inputs are ANDed together with  $\overline{OE1}$  being ECL compatible and  $\overline{OE2}$  being TTL compatible. This offers the choice of controlling the outputs of the device from either a TTL or ECL signal environment.

The SN100KT5540 is characterized for operation from 0°C to  $85^{\circ}$ C.

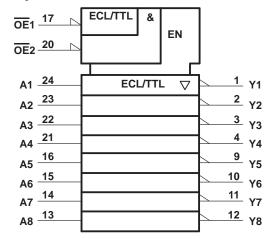
FUNCTION TABLE						
OUT	PUT	DATA	OUTPUT			
ENABLE		INPUT	TTL			
OE1	OE2	Α	Y			
Х	Н	Х	Z			
Н	Х	Х	Z			
L	L	L	Н			
L	L	н	L			

#### FUNCTION TABLE

(TOP VIEW)						
Y1 [ Y2 ] Y3 [ Vcc ] GND [ GND ] GND [ Y5 ] Y6 ] Y7 [ Y8 ]	1 2 3 4 5 6 7 8 9 10 11	24 23 22 21 20 19 18 17 16 15 14	A1 A2 A3 A4 OE2 (TTL) VEE GND OE1 (ECL) A5 A6 A7			
· • L	12	13	1			

#### logic symbol<sup>†</sup>

**R NT PACKAGE** 

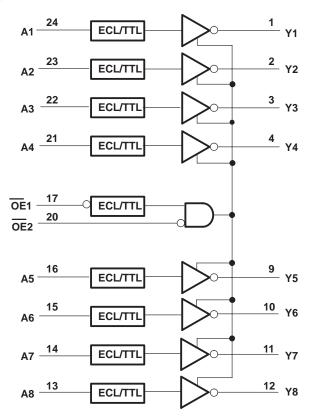


<sup>†</sup> This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.



## logic diagram (positive logic)



#### absolute maximum ratings over operating free-air temperature range (unless otherwise noted)<sup>†</sup>

Supply voltage range, V <sub>CC</sub>	0.5 V to 7 V
Supply voltage range, V <sub>EE</sub>	8 V to 0 V
Input voltage range: TTL (see Note 1)	$\dots \dots -1.2$ V to 7 V
ECL	V <sub>EE</sub> to 0 V
Voltage applied to any output in the high state	$\dots \dots -0.5$ V to V <sub>CC</sub>
Voltage applied to any output in the disabled or power-off state	$\dots \dots -0.5$ V to 5.5 V
Input current range (TTL)	– 30 mA to 5 mA
Current into any output in the low state	96 mA
Operating free-air temperature range	0°C to 85°C
Storage temperature range	–65°C to 150°C

<sup>†</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 1: The TTL input voltage ratings may be exceeded provided the input current ratings are observed.



### recommended operating conditions

		MIN	NOM	MAX	UNIT
VCC	TTL supply voltage	4.5	5	5.5	V
VEE	ECL supply voltage	-4.2	-4.5	-4.8	V
VIH	TTL high-level input voltage	2			V
VIL	TTL low-level input voltage			0.8	V
VIH	ECL high-level input voltage <sup>†</sup>	- 1150		-840	mV
VIL	ECL low-level input voltage <sup>†</sup>	-1810		-1490	mV
IК	TTL input clamp current			-18	mA
ЮН	High-level output current			-15	mA
IOL	Low-level output current			48	mA
TA	Operating free-air temperature	0		85	°C

<sup>†</sup> The algebraic convention, in which the least positive (most negative) value is designated minimum, is used in this data sheet for logic levels only.

# electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER		TEST CONDITIONS		MIN	TYP‡	MAX	UNIT	
VIK	OE2 only	V <sub>CC</sub> = 4.5 V,	$V_{EE} = -4.2 V,$	lj = – 18 mA	1		-1.2	V
		V <sub>CC</sub> = 4.5 V,	$V_{\mbox{\scriptsize EE}}$ = $-4.5$ V $\pm$ 0.3 V,	$I_{OH} = -3 \text{ mA}$	2.4	3.3		
Vон		V <sub>CC</sub> = 4.5 V,	$V_{\mbox{\scriptsize EE}}$ = $-4.5$ V $\pm$ 0.3 V,	I <sub>OH</sub> = – 15 mA	2	3.1		V
VOL		V <sub>CC</sub> = 4.5 V,	$V_{\mbox{\scriptsize EE}}$ = $-4.5$ V $\pm$ 0.3 V,	I <sub>OL</sub> = 48 mA		0.38	0.55	V
lj –	OE2 only	V <sub>CC</sub> = 5.5 V,	$V_{EE} = -4.8 V_{,}$	V <sub>I</sub> = 7 V			0.1	mA
Iн	OE2 only	V <sub>CC</sub> = 5.5 V,	$V_{EE} = -4.8 V_{,}$	V <sub>I</sub> = 2.7 V	1		20	μΑ
١ <sub>IL</sub>	OE2 only	V <sub>CC</sub> = 5.5 V,	$V_{EE} = -4.8 V_{,}$	V <sub>I</sub> = 0.5 V	1		-0.5	mA
Iн	Data inputs and OE1	V <sub>CC</sub> = 5.5 V,	$V_{EE} = -4.8 V_{,}$	VIH = -840 mV	1		350	μΑ
ΙL	Data inputs and OE1	V <sub>CC</sub> = 5.5 V,	$V_{EE} = -4.8 V_{,}$	V <sub>IL</sub> = -1810 mV	0.50			μΑ
IOZH		V <sub>CC</sub> = 5.5 V,	$V_{EE} = -4.8 V_{,}$	V <sub>O</sub> = 2.7 V	1		50	μΑ
IOZL		V <sub>CC</sub> = 5.5 V,	$V_{EE} = -4.8 V,$	V <sub>O</sub> = 0.5 V	1		-50	μΑ
los§		V <sub>CC</sub> = 5.5 V,	$V_{EE} = -4.8 V_{,}$	$V_{O} = 0 V$	-100		-225	mA
Іссн		V <sub>CC</sub> = 5.5 V,	$V_{EE} = -4.8 V$			67	97	mA
ICCL		V <sub>CC</sub> = 5.5 V,	V <sub>EE</sub> = -4.8 V		1	84	120	mA
ICCZ		V <sub>CC</sub> = 5.5 V,	$V_{EE} = -4.8 V$			81	116	mA
IEE		V <sub>CC</sub> = 5.5 V,	$V_{EE} = -4.8 V$			-22	-33	mA
Ci		V <sub>CC</sub> = 5 V,	V <sub>EE</sub> = 4.5 V			5		pF
Co		V <sub>CC</sub> = 5 V,	V <sub>EE</sub> = 4.5 V			7		pF

<sup>‡</sup> All typical values are at V<sub>CC</sub> = 5 V, V<sub>EE</sub> = -4.5 V, T<sub>A</sub> =  $25^{\circ}$ C.

§ Not more than one output should be tested at a time, and the duration of the test should not exceed 10 ms.

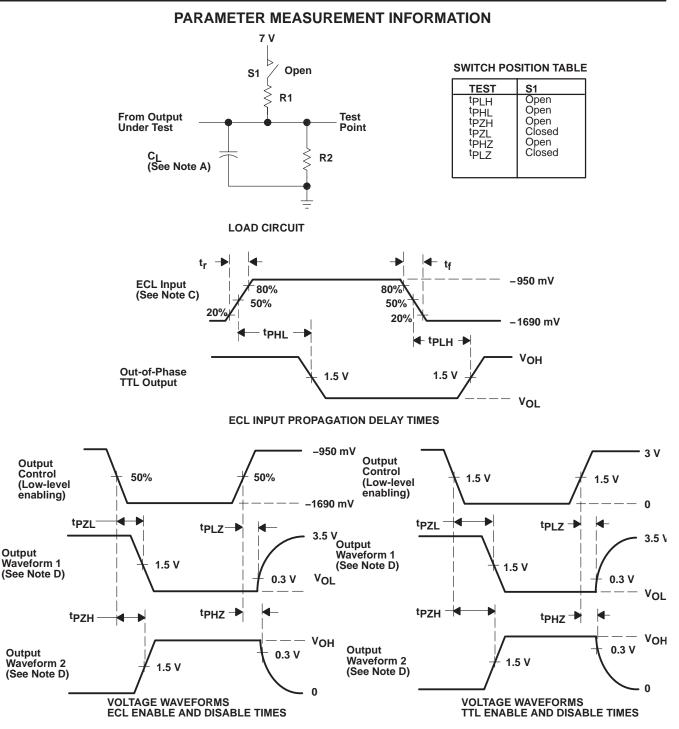


switching characteristics over recommended ranges of supply voltage and operating free-air temperature (see Figure 1)

	FROM	то	C <sub>L</sub> = 50 pF, R1 = 500 Ω,			UNIT
PARAMETER	(INPUT)	(OUTPUT)	R2 = 500 Ω			
			MIN	TYP <sup>†</sup>	MAX	
<sup>t</sup> PLH			1.6	3.9	6.4	
<sup>t</sup> PHL	A	Y	1.6	4.2	6.4	ns
<sup>t</sup> PZH	OE1		2.4	4.5	6.9	
<sup>t</sup> PZL		Y	3.5	5.9	8.7	ns
<sup>t</sup> PHZ		N N	2.8	5.2	8.1	
<sup>t</sup> PLZ	OE1	Y	2.2	4.6	8	ns
<sup>t</sup> PZH			1.4	3.3	6.1	
<sup>t</sup> PZL	OE2	Y	2.5	4.7	7.9	ns
<sup>t</sup> PHZ			1.6	4.1	6.5	
<sup>t</sup> PLZ	OE2	Y	0.7	3.3	6.4	ns

<sup>†</sup> All typical values are at V<sub>CC</sub> = 5 V, V<sub>EE</sub> = -4.5 V, T<sub>A</sub> =  $25^{\circ}$ C.





NOTES: A.C<sub>1</sub> includes probe and jig capacitance.

- B. For TTL inputs, input pulses are supplied by generators having the following characteristics: PRR  $\leq$  10 MHz, Z<sub>0</sub> = 50  $\Omega$ , t<sub>r</sub>  $\leq$  2.5 ns, t<sub>f</sub>  $\leq$  2.5 ns.
- C. For ECL inputs, input pulses are supplied by generators having the following characteristics: PRR  $\leq$  10 MHz, Z<sub>0</sub> = 50  $\Omega$ , t<sub>f</sub>  $\leq$  0.7 ns, t<sub>f</sub>  $\leq$  0.7 ns.
- D. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- E. The outputs are measured one at a time with one transition per measurement.



FIGURE 1. LOAD CIRCUIT AND VOLTAGE WAVEFORMS



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