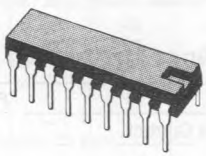
MASK - PROGRAMMABLE SPEECH CIRCUITS

SPEECH CIRCUIT

- MINIMUM NUMBER OF INEXPENSIVE EXTERNAL COMPONENTS, 5 CAPACITORS AND 10 RESISTORS
- MUTE FUNCTION FOR PARALLEL OPERATION WITH DTMF GENERATOR OR DECADING IMPULSING
- LOW VOLTAGE OPERATION, DOWN TO 3.3 V
- VERY SHORT START-UP TIME
- SEPARATE POWER SUPPLY POSSIBLE FOR OUTPUT AMPLIFIER



DIP-18
(Plastic)

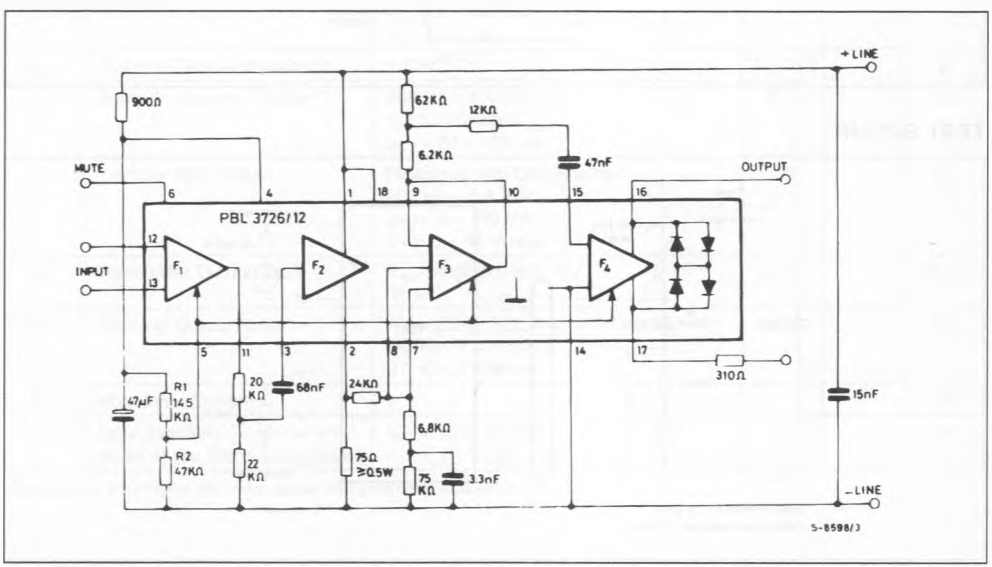
ORDER CODE : PBL3726/12

DESCRIPTON

PBL3726/12 is a standard version of the PBL3726 family of the mask-programmable, monolithic integrated speech circuits for use in electronic telephones. It is designed for use with a low impedance microphone. Sending and receiving gain is regulated with line length. Different ranges of amplifier regulation for various current feeds can be obtained. Typical current feeds as 48 V 2 x 250 Ω 2 x 400 Ω and 36 V 2 x 250 Ω can be handled.

Application-dependent parameters such as line balance, sidetone level and frequency response are set by external components. Parameters are set independently which means easy adaptation for various market needs. An extra 20 dB amplifier can be used for various purposes such as extra receiving gain with volume control or active sidetone balance.

TEST CIRCUIT



ABSOLUTE MAXIMUM RATINGS

(Maximum Ratings over Operating Free-air Temperature Range unless otherwise stated)

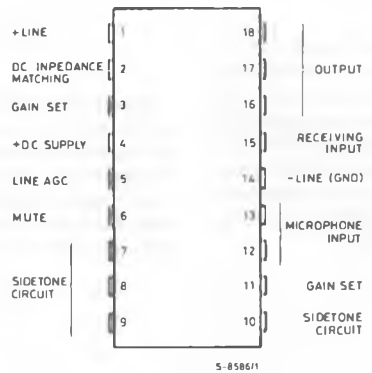
Symbol	Parameter	Test Conditions	Unit
V_{DC}	Line Voltage, t_p = 2 s	22	V
I_{DC} (*)	Continuous Operating Line Current	100	mA
T_j	Junction Temperature	150	°C
T_{amb}	Operating Ambient Temperature	– 40 to + 70	°C
T_{stg}	Storage Temperature	– 55 to + 150	°C

(*) Max. current increases linearly up to 130 mA with max operating temperature lowered to + 55 °C.

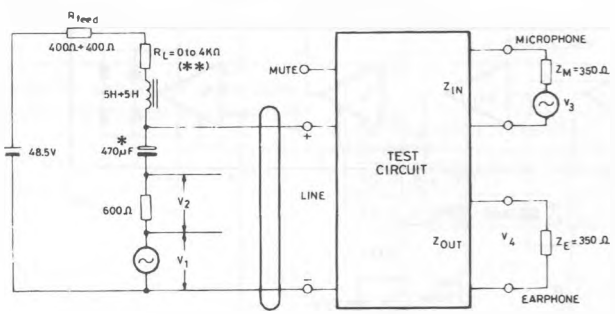
RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Min.	Typ.	Max.	Unit
I_L	Line Current	15		100	mA

CONNECTION DIAGRAM



TEST SET-UP



THERMAL DATA

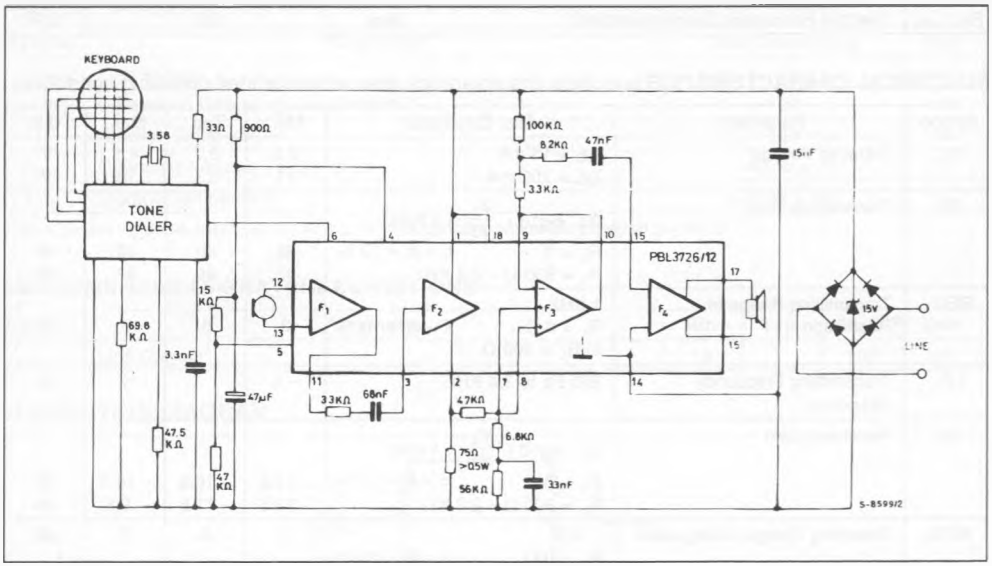
$R_{th\ j\text{-}amb}$	Thermal Resistance Junction-ambient	Max	80	°C/W

ELECTRICAL CHARACTERISTICS (electrical characteristics over recommended operating conditions)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V_D	Terminal Voltage	I_{DC} = 15 mA I_{DC} = 100 mA	3.3 11	3.7 13	4.1 15	V V
G_T	Transmitting Gain *	$20 . \log10 (\frac{V_2}{V3})$ 1 KHz R_L = 0 E = E + 10 % R_L = 900 Ω – 2.2 KHz	 38 43	 40 45	 42 47	 dB dB
REG_T	Transmitting Range of Regulation	1 KHz R_L = 0 Ω E = E + 10 % to R_L = 900 Ω	3	5	7	dB
Lin_T	Transmitting Frequency Response	200 Hz to 3.4 KHz	– 1		1	dB
G_R	Receiving Gain *	$20 . \log10 (\frac{V_4}{V1})$ 1 KHz R_L = 0 Ω E = E + 10 % R_L = 900 Ω – 2.2 KΩ	 – 18.5 – 13.5	 – 16.5 – 11.5	 – 14.5 – 9.5	 dB dB
REG_R	Receiving Range of Regulation	1 KHz R_L = 0 Ω E = E + 10 % to R_L = 900 Ω	3	5	7	dB
Lin_R	Receiving Frequency Response	200 Hz to 3.4 KHz	– 1		1	dB
Z_{IN}	Transmitter Input Impedance	1 KHz		2.5		KΩ
V_T	Transmitter Dynamic Output	200 Hz – 3.4 KHz ≤ 2 % Distortion I_{DC} = 20 – 100 mA		1.4		V_p
V_T	Transmitter Max Output	200 Hz – 3.4 KHz I_{DC} = 0 – 100 mA V_3 = 0 – 1 V		3		V_p
Z_{OUT}	Receiver Output Impedance	1 KHz		3 + 310		Ω
V_R	Receiver Dynamic Output **	200 Hz – 3.4 KHz ≤ 2 % Distortion I_{DC} = 20 – 100 mA		0.4		V_p
V_R	Receiver Max Output	Measured with Line Rectifier 200 Hz – 3.4 KHz I_{DC} = 0 – 100 mA V_1 = 0 – 50 V		0.9		V_p
N_T	Transmitter Output Noise	P_{sof}-weighted, REL 1 V R_L = 0		– 75		dB_{psof}
N_R	Receiver Output Noise	A-weighted, REL 1 V, with Cable 0-5 Km ∅ 0.5 mm ; 0-3 Km ∅ 0.4 mm		– 85		dB_A
I_M	Mute Input Current		0.1			mA
I_{DC}	Extra Available Current when Muted at the Same DC-voltage	I_{DC} = 15 – 100 mA		10		mA

* Adjustable to both higher and lower values with external components.

Figure 1 : Typical Application.



Some typical values for R1 and R2 for some different supplies from telephone stations are shown in the next table.

Type	R1	R2
No Regulation, all Feeding Systems	∞	0
48 V, 2 x 400 Ω	14.5 kΩ	47 kΩ
48 V, 2 x 200 Ω	18 kΩ	47 kΩ