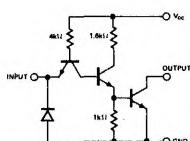


S5405-A,F,W • N7405-A,F

DIGITAL 54/74 TTL SERIES

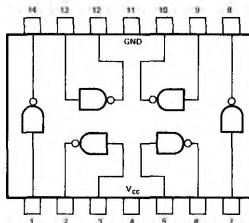
### SCHEMATIC (each inverter)



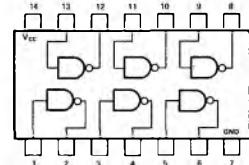
NOTE: Component values shown are nominal.

### PIN CONFIGURATIONS

#### W PACKAGE



#### A,F PACKAGE



### RECOMMENDED OPERATING CONDITIONS

	MIN	NOM	MAX	UNIT
Supply Voltage $V_{CC}$ : S5405 Circuits	4.5	5	5.5	V
N7405 Circuits	4.75	5	5.25	V
Normalized Fan-Out from Output, N			10	
Operating Free-Air Temperature Range, $T_A$ : S5405 Circuits	-55	25	125	°C
N7405 Circuits	0	25	70	°C

### ELECTRICAL CHARACTERISTICS (over recommended operating free-air temperature range unless otherwise noted)

PARAMETER	TEST CONDITIONS*	MIN	TYP**	MAX	UNIT
$V_{in(1)}$	$V_{CC} = \text{MIN}$		2		V
$V_{in(0)}$	$V_{CC} = \text{MIN}$		0.8		V
$I_{out(1)}$	$V_{CC} = \text{MIN}$ , $V_{out(1)} = 5.5V$		250		$\mu\text{A}$
$V_{out(0)}$	$V_{CC} = \text{MIN}$ , $I_{sink} = 16\text{mA}$		0.4		V
$I_{in(0)}$	$V_{CC} = \text{MAX}$ ,		-1.6		mA
$I_{in(1)}$	$V_{CC} = \text{MAX}$ , $V_{CC} = \text{MAX}$ , $V_{in} = 0.4V$		40		$\mu\text{A}$
$I_{CC(0)}$	$V_{CC} = 5V$ , $T_A = 25^\circ\text{C}$	18	33		mA
$I_{CC(1)}$	$V_{CC} = 5V$ , $T_A = 25^\circ\text{C}$	6	12		mA

SWITCHING CHARACTERISTICS,  $V_{CC} = 5V$ ,  $T_A = 25^\circ C$ 

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
$t_{pd0}$	$C_L = 15\text{pF}$ , Propagation delay time to logical 0 level		8	15	ns
$t_{pd1}$	$C_L = 15\text{pF}$ , Propagation delay time to logical 1 level		40	55	ns

- For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions for the applicable device type.
- All typical values are at  $V_{CC} = 5V$ ,  $T_A = 25^\circ C$