MC3100/MC3000 series

8-INPUT "NAND" GATE

MC3116F • MC3016F MC3116L • MC3016L,P (54H30J) (74H30J, N)

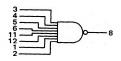
2.8 k 760 58

3 0 4.0 k 58

6 0 11 0 12 0 1 0 2 0 0 7

7 Gnd

This device is an 8-input NAND gate. It is useful when processing a large number of variables, such as in encoders and decoders.



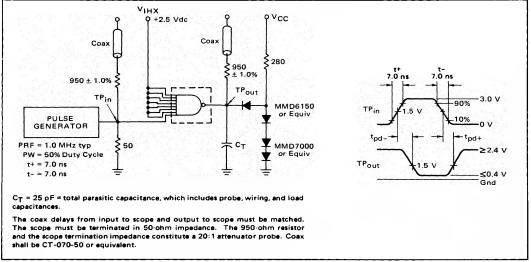
Positive Logic: 8 = 3 • 4 • 5 • 6 • 11 • 12 • 1 • 2 Negative Logic: 8 = 3 + 4 + 5 + 6 + 11 + 12 + 1 + 2

Input Loading Factor = 1
Output Loading Factor = 10
Total Power Dissipation = 22 mW typ/pkg
Propagation Delay Time = 8.0 ns typ

Pin numbers for the 54H30F/74H30F device are shown in the chart. These devices are available on special request.

DEVICE						PIN	NU	MBE	RS					
MC3116F,L/3016F,L,P	1	2	3	4	5	6	7	8	9	10	11	12	13	14
54H30F/74H30F	9	10	2	3	5	6	11	12	1	14	7	8	13	4

SWITCHING TIME TEST CIRCUIT AND WAVEFORMS

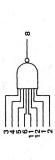


General Information section for packaging.

MC3116, MC3016 (continued)

ELECTRICAL CHARACTERISTICS

Test procedures are shown for only one input of this device. To complete testing, sequence through remaining inputs in the same manner.



															_					=	ST CIR	RENT/VO	TEST CURRENT/VOLTAGE VALUES	E					
														@ Tect	L		μM		H					Volts					
													-	Temperature			_₹	ا <u>.</u> ا	× 4	>=	>"	>"	> Ha	Vmax	> ×	CCC.	V _{CCH}	NHX NHX	
														_		202	-2.0	,	1.1	22	0 0.4	2.4	4.0		5.0	4.5	5.5		
													MC3116	~	1	20 -2	-2.0 1	1.0 -1	-10 1.1	1 1.8	8 0.4	2.4	4.0	7.0	0 2.0	4.5	5.5	2.5	
														(+12	+125°C	202	-2.0	1	-	0.8 1.8	8 0.4	2.4	4.0	-	5.0	4.5	5.5	1	
														_	٥,0	20 -2	-2.0	,	1.1	1 2.0	0.4	2.5	4.0	-	5.0	4.75	5.25		
													MC3016	~		20	-2.0	1.0 -1	-10 1.1	1 1.8	8 0.4	2.5	4.0	7.0	0 2.0	4.75	5.25	2.5	_
														+	+75°C	20 -2	-2.0		6.0	9 1.8	8 0.4	2.5	4.0	'	5.0	4.75	5.25		
		Pin		MC	MC3116 Test Li	est Limits	ts			MC3	MC3016 Test Limits	t Limits						-	EST CU	RRENT	/ VOLTA	GE APPL	EST CURRENT / VOLTAGE APPLIED TO PINS LISTED BELOW	LISTED	BELOW:				
		Under	-5	−55°C	+25°C	ر ئ	+125°C	2,℃	٥,٥		+25°C	ں	+75°C		1	-	1	F.	1	1:	1:	;	:	1:	1		-	-	
Characteristic	Symbol	Test	Min	Min Max	Min	Max	Min Max	Max	Win	Max	Win	Max	Min M	Max U	Unit	10,	Ŧ	o u	>=	×=	>"	>"	Y.	Vmax) VCC	CCL	V _{CCH}	ν H V	Gnd
Input Forward Current	I.	8		-2.0		-2.0		-2.0		-2.0	-	-2.0	-	-2.0 m	mAdc	-		,	-	<u>'</u>	6		1, 2, 4, 5, 6, 11, 12	2,51	1	,	14	<u> </u>	7
Leakage Current	IR.	8		20	, .	20	,	20	1	20		20	,	20 μ/	μAdc	-	1	1		-	'	8		'	-	1	14		1,2,4,5, 6,7,11,12
Breakdown Voltage	BVin	6	1	,	5.5						5.5		,	>	Vdc			60		<u> </u>		1		'	1		14	i.	1, 2, 4, 5, 6, 7, 11, 12
Clamp Voltage	v _D	8				-1.5	,					-1.5	,	>	Vdc	-	,	,		-	,	'		'	-	14			-
Output Output Voltage	N _{OL}	80	,	0.4	- 1	0.4	,	0.4		9.0	,	0.4	0	0.4 V	Vdc		-	-		8	-	'	1,2,4,5	52	'	14	,		-
	но л	8	2.4		2.4	-	2.4		2.5		2.5		2.5	>	Vdc		80	· ·	6	<u>'</u>		,	1, 2, 4, 5, 6, 11, 12	2,2	,	14	,	-	4
Short-Circuit Current	¹sc	80	-40	-100	-40	-100	-40	-100	-40	-100	-40	-100	-40 -1	-100 m/	mAdc				,	-	•	1		'	'		14		1,2,3,4,5,8,8,11,12
Power Requirements (Total Device) Maximum Power Supply Current	Imax	14				6.5	1	1	ı	,	,	6.5	,	Ĭ.	mAdc		1	1			,		,	14	'	,			1, 2, 3, 4, 5, 7, 11, 12
Power Supply Drain	нач	14	1	10	,	10	,	10		10	,	10	-	10 m/	mAdc	,				-		,	1,2,3,4, 5,6,11,12	12	'	,	14	,	4
	IPDL	14		4.2	,	4.2		4.2	1	4.2	,	4.2	4	4.2 m/	mAdc			'	'	'	,			'	-		14		1,2,3,4,5,7,11,12
Switching Parameters						,						,	-			0	Pulse	-		-	-							1,2,4,5,	
Turn-On Delay	-pd	3,8		ı'	,	12		,			,	12			ns	8	8			'			-	_	**	-	-	6, 11, 12	
Turn-Off Delay	tnd+	3,8		1	,	10	,	,			,	10	,	,	su	3	8		-	_	,	-	-	_	14	•		1,2,4,5,	7