

Product Overview

74LVC245: Octal Transceiver, CMOS, Low Voltage

For complete documentation, see the data sheet.

The 74LVC245A is a high performance, non-inverting octal transceiver operating from a 1.2 to 5.5V supply. High impedance TTL compatible inputs significantly reduce current loading to input drivers while TTL compatible outputs offer improved switching noise performance. A V_I specification of 5.5V allows 74LVC245A inputs to be safely driven from 5V devices. The 74LVC245A is suitable for memory address driving and all TTL level bus oriented transceiver applications.

Current drive capability is 24mA at both A and B ports. The Transmit/Receive (T/Rbar) input determines the direction of data flow through the bi-directional transceiver. Transmit (active-HIGH) enables data from A ports to B ports; Receive (active-LOW) enables data from B to A ports. The Output Enable input, when HIGH, disables both A and B ports by placing them in a HIGH Z condition.

Features

- Designed for 1.2 to 3.6 V V_{CC} Operation
- 5 V Tolerant - Interface Capability with 5 V TTL Logic
- Supports Live Insertion and Withdrawal
- I_{OFF} Specification Guarantees High Impedance When $V_{CC} = 0$ V
- 24 mA Output Sink and Source Capability
- Near Zero Static Supply Current in All Three Logic States (10 μ A) Substantially Reduces System Power Requirements
- Latch-up Performance Exceeds 250 mA
- ESD Performance: Human Body Model >2000 V; Machine Model >200 V
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

Part Electrical Specifications

Product	Compliance	Status	Channels	Output	V_{CC} Min (V)	V_{CC} Max (V)	t_{pd} Max (ns)	I_O Max (mA)	Package Type
74LVC245ADTR2G	Pb-free Halide free	Active	8	3-State	1.2	3.6	6.3	24	TSSOP 20 LEAD

For more information please contact your local sales support at www.onsemi.com.

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