

54ACT/74ACT2725

512 x 9 First In, First Out Memory (FIFO)

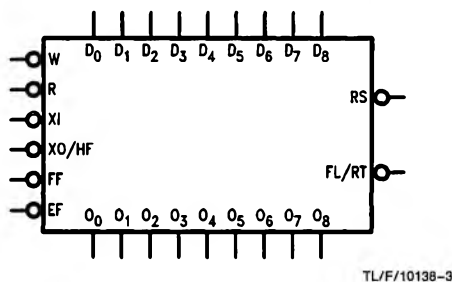
General Description

The 512 x 9 FIFO is a first-in, first-out dual port memory capable of asynchronous, simultaneous read and write. Other important features are: expansion capability in both the word depth and bit width, half-full flag capability in the single device mode, empty and full warning flags, ring pointers for fail-through time; it is suited for high-speed applications.

Features

- First-in, first-out dual port memory
- 512 x 9 organization
- Low power consumption
- Asynchronous and simultaneous read and write
- Fully expandable by word depth and/or bit width
- Half-full flag capability in single device mode
- Empty and full warning flags
- Auto retransmit capability
- Outputs source/sink 8 mA
- 'ACT2725 has TTL-compatible inputs
- Pin and functionality compatible with IDT7201A

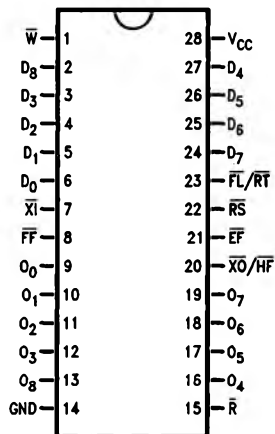
Logic Symbol



TL/F/10138-3

Connection Diagram

Pin Assignment
for DIP, Flatpak and SOIC



TL/F/10138-1

Pin Names	Description
D ₀ -D ₈	Data Inputs
O ₀ -O ₈	Data Outputs
W	Write Enable
R	Read Enable
XI	Expansion In
XO/HF	Expansion Out, Half-Full Flag
EF	Empty Flag
FF	Full Flag
RS	Reset
FL/RT	First Load/Retransmit