

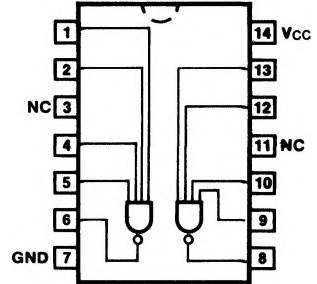
# 54S/74S140

## DUAL 4-INPUT NAND LINE DRIVER

**CONNECTION DIAGRAM**  
PINOUT A

**ORDERING CODE:** See Section 9

PKGS	PIN OUT	COMMERCIAL GRADE	MILITARY GRADE	PKG TYPE
		V <sub>CC</sub> = +5.0 V ±5%, T <sub>A</sub> = 0° C to +70° C	V <sub>CC</sub> = +5.0 V ±10%, T <sub>A</sub> = -55° C to +125° C	
Plastic DIP (P)	A	74S140PC		9A
Ceramic DIP (D)	A	74S140DC	54S140DM	6A
Flatpak (F)	A	74S140FC	54S140FM	3I



**INPUT LOADING/FAN-OUT:** See Section 3 for U.L. definitions

PINS	54/74S (U.L.) HIGH/LOW
Inputs	2.5/2.5
Outputs	75/37.5

**DC AND AC CHARACTERISTICS:** See Section 3\*

SYMBOL	PARAMETER	54/74S		UNITS	CONDITIONS
		Min	Max		
V <sub>OH</sub>	Output HIGH Voltage	2.0		v	V <sub>CC</sub> = Min, V <sub>IN</sub> = 0.5 V, R <sub>0</sub> = 50 Ω to Gnd
V <sub>OL</sub>	Output LOW Voltage		0.5	v	V <sub>CC</sub> = Min, I <sub>OL</sub> = 60 mA V <sub>IN</sub> = 2.0 V
I <sub>OS</sub>	Output Short Circuit Current	-50	-225	mA	V <sub>CC</sub> = Max, V <sub>OUT</sub> = 0 V
I <sub>CCH</sub> I <sub>CCL</sub>	Power Supply Current		18 44	mA	V <sub>IN</sub> = Gnd V <sub>IN</sub> = Open V <sub>CC</sub> = Max
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay		6.5 6.5	ns	Figs. 3-1, 3-4

\*DC limits apply over operating temperature range; AC limits apply at T<sub>A</sub> = +25° C and V<sub>CC</sub> = +5.0 V.