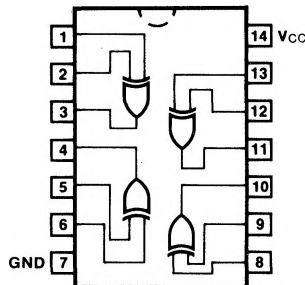


# 54LS/74LS266

## QUAD 2-INPUT EXCLUSIVE-NOR GATE

(With Open-Collector Outputs)

### CONNECTION DIAGRAM PINOUT A



**ORDERING CODE:** See Section 9

PKGS	PIN OUT	COMMERCIAL GRADE	MILITARY GRADE	PKG TYPE
		$V_{CC} = +5.0\text{ V} \pm 5\%$ , $T_A = 0^\circ\text{C to } +70^\circ\text{C}$	$V_{CC} = +5.0\text{ V} \pm 10\%$ , $T_A = -55^\circ\text{C to } +125^\circ\text{C}$	
Plastic DIP (P)	A	74LS266PC		9A
Ceramic DIP (D)	A	74LS266DC	54LS266DM	6A
Flatpak (F)	A	74LS266FC	54LS266FM	3I

### TRUTH TABLE

INPUTS		OUTPUT
A	B	Z
L	L	H
L	H	L
H	L	L
H	H	H

H = HIGH Voltage Level  
L = LOW Voltage Level

**INPUT LOADING/FAN-OUT:** See Section 3 for U.L. definitions

PINS	54/74LS (U.L.) HIGH/LOW
Inputs	1.0/0.375
Outputs	OC**/5.0 (2.5)

**DC AND AC CHARACTERISTICS:** See Section 3\*

SYMBOL	PARAMETER	54/74LS		UNITS	CONDITIONS
		Min	Max		
$I_{CC}$	Power Supply Current		13	mA	$V_{CC} = \text{Max}$
$t_{PLH}$ $t_{PHL}$	Propagation Delay		23 23	ns	Other Input LOW Figs. 3-2, 3-5
$t_{PLH}$ $t_{PHL}$	Propagation Delay		23 23	ns	Other Input HIGH Figs. 3-2, 3-5

\*DC limits apply over operating temperature range; AC limits apply at  $T_A = +25^\circ\text{C}$  and  $V_{CC} = +5.0\text{ V}$ .

\*\*OC — Open Collector