

54FCT/74FCT845A • 54FCT/74FCT845B

8-Bit Transparent Latch with TRI-STATE® Outputs

General Description

The 'FCT845A/B bus interface latch is designed to eliminate the extra packages required to buffer existing latches and provide easy expansion through multiple \overline{OE} controls.

FACT™ FCTA/B utilizes NSC quiet series technology to provide improved quiet output switching and dynamic threshold performance.

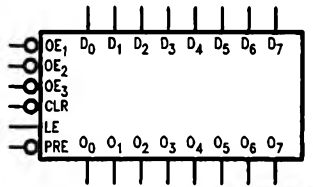
FACT FCTA features GTO™ output control and undershoot corrector in addition to a split ground bus for superior performance.

FACT FCTB features an undershoot corrector in addition to a split ground bus for superior performance.

Features

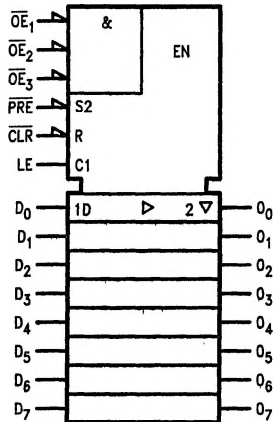
- NSC 54FCT/74FCT845A/B is pin and functionally equivalent to IDT 54FCT/74FCT845A/B
- High speed parallel latches
- Buffered common latch enable, clear and preset input
- Input clamp diodes to limit bus reflections
- TTL/CMOS input and output level compatible
- $I_{OL} = 48 \text{ mA (Com)}, 32 \text{ mA (Mil)}$
- CMOS power levels
- 4 kV minimum ESD Immunity
- Military product compliant to MIL-STD 883

Logic Symbols



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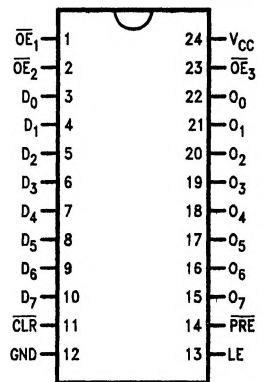
Pin Names	Description
D ₀ -D ₇	Data Inputs
O ₀ -O ₇	Data Outputs
\overline{OE}_1 - \overline{OE}_3	Output Enables
LE	Latch Enable
\overline{CLR}	Clear
\overline{PRE}	Preset



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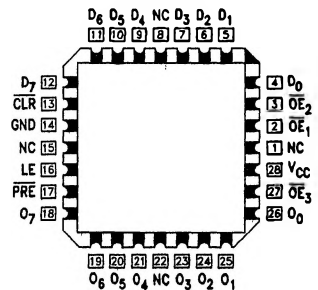
Connection Diagrams

Pin Assignment for DIP, Flatpak and SOIC



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Pin Assignment for LCC



TL/F/10683-4