

54FCT/74FCT821A • 54FCT/74FCT821B

10-Bit D Flip-Flop with TRI-STATE® Outputs

General Description

The 'FCT821A/B is a 10-bit D flip-flop with TRI-STATE outputs arranged in a broadside pinout.

FACT™ FCTA/B utilizes NSC quiet series technology to provide improved quiet output switching and dynamic threshold performance.

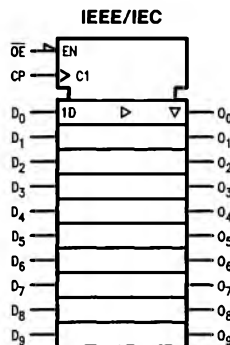
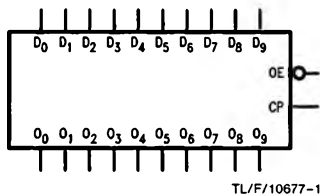
FACT FCTA features GTO™ output control and undershoot corrector in addition to a split ground bus for superior performance.

FACT FCTB features an undershoot corrector in addition to a split ground bus for superior performance.

Features

- NSC 54FCT/74FCT821A/B is pin and functionally equivalent to IDT 54FCT/74FCT821A/B
- High-speed parallel registers with positive edge-triggered D-type flip-flops for ringing suppression
- Buffered common clock enable (EN) and asynchronous clear input ($\overline{\text{CLR}}$)
- Input clamp diodes for ringing suppression
- TTL/CMOS input and output level compatible
- $I_{OL} = 48 \text{ mA (COM)}, 32 \text{ mA (MIL)}$
- CMOS power levels
- 4 kV minimum ESD immunity
- Military Product compliant to MIL-STD 883
- TRI-STATE outputs for bus interfacing
- Noninverting outputs

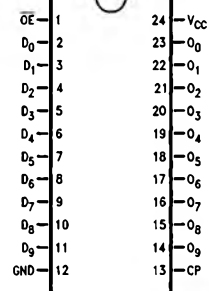
Logic Symbols



| Pin Names | Description |
|------------------------|---------------------|
| D_0 - D_9 | Data Inputs |
| O_0 - O_9 | Data Outputs |
| $\overline{\text{OE}}$ | Output Enable Input |
| CP | Clock Input |

Connection Diagrams

Pin Assignment for DIP, Flatpak and SOIC



Pin Assignment for LCC

